

## AP1302



# Image Signal Processor, 13 MP

AP1302 is a imaging co-processor capable of processing up to 13 MP ON Semiconductor primary camera as well as a front facing camera. AP1302 has the ability to handle all image processing functions including interlaced High Dynamic Range (iHDR) operations

- Primary camera interface
- Primary camera input format
- Output interface
- Output format
- Secondary camera interface
- Maximum resolution
- Maximum frame rate
- Maximum colour processing frequency
- ISP capabilities on chip
- Digital features
- Up to 4-lane MIPI (1.2 Gbps / lane)
- RAW6, RAW8
- Up to 4-lane MIPI (1.2 Gbps / lane)
- YUV422, YUV420, 888RGB, 565RGB, 555RGB, JPEG, RAW8, RAW10, RAW12, MJPEG
- Up to 3-lane MIPI (1.2 Gbps / lane)
- 4224 x 3156 (13Mp)
- 30 fps at 13MP; 120 fps at 1080p
- 540 Mpixels / second
- Adaptive tone mapping, local tone mapping
- Frame rate reduction, image resizing, face detection, digital zoom / pan, OSD

For more features, see the data sheet

- Image Signal Processing
- IoT
- Drones
- Machine Vision

	Pricing (\$/Unit)	Compliance	Status	Frame Rate (fps)	Video	Output Format	Compatible Sensors	Package Type
AP1302CSSL00 SMGA0-DR		 	Active	30			AR1331	VFPGA-120