

製品概要

MC74AC4040: 12-Stage Binary Ripple Counter

技術情報は、データシートをご参照ください。

The MC74AC4040 consists of 12 master-slave flip-flops. The output of each flip-flop feeds the next and the frequency at each output is half that of the preceding one. The state of the counter advances on the negative-going edge of the Clock input. Reset is asynchronous and active-high. State changes of the Q outputs do not occur simultaneously because of internal ripple delays. Therefore, decoded output signals are subject to decoding spikes and may have to be gated with the Clock of the MC74AC4040 for some designs.

特長

- 140 MHz Typ. Clock
- Outputs Source/Sink 24 mA
- Operating Voltage Range: 2.0 to 6.0 V
- High Noise Immunity
- These devices are available in Pb-free package(s). Specifications herein apply to both standard and Pb-free devices. Please see our website at www.onsemi.com for specific Pb-free orderable part numbers, or contact your local ON Semiconductor sales office or representative.

電氣的仕様

製品	Compliance	Status	Type	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	P _D Max (W)	I _O Max (mA)	Package Type
MC74AC4040DG	Pb-free	Active	Counter	2	6	8	0.75	24	SOIC-16
	Halide free								
MC74AC4040DR2G	Pb-free	Active	Counter	2	6	8	0.75	24	SOIC-16
	Halide free								

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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