

## 製品概要

## MC74HC04A: Hex Inverter

技術情報は、データシートをご参照ください。

High-Performance Silicon-Gate CMOS The MC74HC04A is identical in pinout to the LS04 and the MC14069. The device inputs are compatible with Standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs. The device consists of six three-stage inverters.

## 特長

- Output Drive Capability: 10 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS and TTL
- Operating Voltage Range: 2 to 6V
- Low Input Current: 1mA
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance With the JEDEC Standard No. 7A Requirements
- Chip Complexity: 36 FETs or 9 Equivalent Gates
- Pb-Free Packages are Available

## 電氣的仕様

製品	Compliance	Status	Channels	Output	V <sub>CC</sub> Min (V)	V <sub>CC</sub> Max (V)	t <sub>pd</sub> Max (ns)	I <sub>O</sub> Max (mA)	Package Type
MC74HC04ADG	Pb-free	Active	6	CMOS	2	6	19	5.2	SOIC-14
	Halide free								
MC74HC04ADR2G	Pb-free	Active	6	CMOS	2	6	19	5.2	SOIC-14
	Halide free								
MC74HC04ADTR2G	Pb-free	Active	6	CMOS	2	6	19	5.2	TSSOP-14
	Halide free								
NLV74HC04ADG	AEC Qualified	Active	6	CMOS	2	6	19	5.2	SOIC-14
	PPAP Capable								
	Pb-free								
	Halide free								
NLV74HC04ADR2G	AEC Qualified	Active	6	CMOS	2	6	19	5.2	SOIC-14
	PPAP Capable								
	Pb-free								
	Halide free								
NLV74HC04ADTR2G	AEC Qualified	Active	6	CMOS	2	6	19	5.2	TSSOP-14
	PPAP Capable								
	Pb-free								
	Halide free								

詳細は、弊社 [www.onsemi.jp](http://www.onsemi.jp) の営業または販売代理店にお問い合わせください。

8/18/2019 作成