

製品概要

MC74HCT365A: Hex buffer/line driver, 3-state, TTL

技術情報は、データシートをご参照ください。

The MC74HCT365A is identical in pinout to the LS365. The device inputs are compatible with LSTTL outputs. This device is a highspeed hex buffer with 3state outputs and two common activelow Output Enables. When either of the enables is high, the buffer outputs are placed into highimpedance states. The HCT365A has noninverting outputs.

特長

- Output Drive Capability: 15 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 4.5 to 5.5 V
- Low Input Current: 1.0 A
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 90 FETs or 22.5 Equivalent Gates
- These are PbFree Devices

電氣的仕様

製品	Compliance	Status	Channels	Output	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HCT365ADG	Pb-free	Active	6	3-State	4.5	5.5	12	6	SOIC-16
	Halide free								
MC74HCT365ADR2G	Pb-free	Active	6	3-State	4.5	5.5	12	6	SOIC-16
	Halide free								
MC74HCT365ADTG	Pb-free	Active	6	3-State	4.5	5.5	12	6	TSSOP-16
	Halide free								
MC74HCT365ADTR2G	Pb-free	Active	6	3-State	4.5	5.5	12	6	TSSOP-16
	Halide free								

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