

製品概要

MC74HCT366A: Hex buffer/line driver, 3-state, inverting, TTL

技術情報は、データシートをご参照ください。

The MC74HCT366A is identical in pinout to the LS366. The device inputs are compatible with standard CMOS or LSTTL outputs. This device is a high-speed hex buffer with 3-state outputs and two common active-low output enables. When either of the enables is high, the buffer outputs are placed into high-impedance states. The HCT366A has inverting outputs.

特長

- Output Drive Capability: 15 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 2.0 to 6.0 V
- Low Input Current: 1.0 A
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 90 FETs or 22.5 Equivalent Gates
- These are PbFree Devices

アプリケーション

- White Goods, Industrial, Desktop, etc

電氣的仕様

製品	Compliance	Status	Channels	Output	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74HCT366ADG	Pb-free Halide free	Active	6	3-State	4.5	5.5	24	6	SOIC-16
MC74HCT366ADR2G	Pb-free Halide free	Active	6	3-State	4.5	5.5	24	6	SOIC-16
MC74HCT366ADTG	Pb-free Halide free	Active	6	3-State	4.5	5.5	24	6	TSSOP-16
MC74HCT366ADTR2G	Pb-free Halide free	Active	6	3-State	4.5	5.5	24	6	TSSOP-16
NLV74HCT366ADTRG	Pb-free Halide free	Active	6	3-State	4.5	5.5	24	6	TSSOP-16
NLVHCT366ADTR2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	6	3-State	4.5	5.5	24	6	TSSOP-16

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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