

## 製品概要

### MC74HC374A: Octal D Flip-Flop

技術情報は、データシートをご参照ください。

High-Performance Silicon-Gate CMOS The MC74HC374A is identical in pinout to the LS374. The device inputs are compatible with standard CMOS outputs; with pullup resistors, they are compatible with LSTTL outputs. Data meeting the setup time is clocked to the outputs with the rising edge of the clock. The Output Enable input does not affect the states of the flip-flops, but when Output Enable is high, the outputs are forced to the high-impedance state; thus, data may be stored even when the outputs are not enabled. The HC374A is identical in function to the HC574A which has the input pins on the opposite side of the package from the output. This device is similar in function to the HC534A which has inverting outputs.

### 特長

- Output Drive Capability: 15 LSTTL Loads
- Outputs Directly Interface to CMOS, NMOS, and TTL
- Operating Voltage Range: 2.0 to 6.0 V
- Low Input Current: 1.0 mA
- High Noise Immunity Characteristic of CMOS Devices
- In Compliance with the Requirements Defined by JEDEC Standard No. 7A
- Chip Complexity: 266 FETs or 66.5 Equivalent Gates
- Pb-Free Packages are Available\*

### 電氣的仕様

製品	Compliance	Status	Type	Channels	V <sub>CC</sub> Min (V)	V <sub>CC</sub> Max (V)	t <sub>pd</sub> Max (ns)	I <sub>O</sub> Max (mA)	Package Type
MC74HC374ADTG	Pb-free Halide free	Active	D-Type	8	2	6	25	6	TSSOP-20
MC74HC374ADTR2G	Pb-free Halide free	Active	D-Type	8	2	6	25	6	TSSOP-20
MC74HC374ADWG	Pb-free Halide free	Active	D-Type	8	2	6	25	6	SOIC-20W
MC74HC374ADWR2G	Pb-free Halide free	Active	D-Type	8	2	6	25	6	SOIC-20W
NLV74HC374ADTR2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	D-Type	8	2	6	25	6	TSSOP-20
NLV74HC374ADWG	AEC Qualified PPAP Capable Pb-free Halide free	Active	D-Type	8	2	6	25	6	SOIC-20W
NLV74HC374ADWR2G	AEC Qualified PPAP Capable Pb-free Halide free	Active	D-Type	8	2	6	25	6	SOIC-20W

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