

製品概要

MC14543B: BCD-to-7-Segment Latch/Decoder/Driver for Liquid Crystals

技術情報は、データシートをご参照ください。

The MC14543B BCD-to-seven segment latch/decoder/driver is designed for use with liquid crystal readouts, and is constructed with complementary MOS (CMOS) enhancement mode devices. The circuit provides the functions of a 4-bit storage latch and an 8421 BCD-to-seven segment decoder and driver. The device has the capability to invert the logic levels of the output combination. The phase (Ph), blanking (Bl), and latch disable (LD) inputs are used to reverse the truth table phase, blank the display, and store a BCD code, respectively. For liquid crystal (LC) readouts, a square wave is applied to the Ph input of the circuit and the electrically common backplane of the display. The outputs of the circuit are connected directly to the segments of the LC readout. For other types of readouts, such as light-emitting diode (LED), incandescent, gas discharge, and fluorescent readouts, connection diagrams are given on this data sheet. Applications include instrument (e.g., counter, DVM etc.) display driver, computer/calculator display driver, cockpit display driver, and various clock, watch, and timer uses.

特長

- Latch Storage of Code
- Blanking Input
- Readout Blanking on All Illegal Input Combinations
- Direct LED (Common Anode or Cathode) Driving Capability
- Supply Voltage Range = 3.0 V to 18 V
- Capable of Driving Two Low-power TTL Loads, One Low-power Schottky TTL Load or Two HTL Loads Over the Rated Temperature Range
- Pin-for-Pin Replacement for CD4056A (with Pin 7 Tied to VSS).
- Chip Complexity: 207 FETs or 52 Equivalent Gates
- Pb-Free Packages are Available

電氣的仕様

| 製品 | Pricing (\$/Unit) | Compliance | Status | Type | Channels | V _{CC} Min (V) | V _{CC} Max (V) | t _{pd} Max (ns) | I _O Max (mA) | Package Type |
|---------------|-------------------|---|--------|-------|----------|-------------------------|-------------------------|--------------------------|-------------------------|--------------|
| MC14543BDG | 0.1356 | Pb-free | Active | Latch | 1 | 3 | 18 | 660 | 10 | SOIC-16 |
| | | Halide free | | | | | | | | |
| MC14543BDR2G | 0.1467 | Pb-free | Active | Latch | 1 | 3 | 18 | 660 | 10 | SOIC-16 |
| | | Halide free | | | | | | | | |
| NLV14543BDR2G | 0.2933 | AEC Qualified PPAP Capable Pb-free Halide free | Active | Latch | 1 | 3 | 18 | 660 | 10 | SOIC-16 |

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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