

製品概要

MC14559B: Successive Approximation Register

技術情報は、データシートをご参照ください。

The MC14549B and MC14559B successive approximation registers are 8 bit registers providing all the digital control and storage necessary for successive approximation analog to digital conversion systems. These parts differ in only one control input. The Master Reset (MR) on the MC14549B is required in the cascaded mode when more than 8 bits are desired. The Feed Forward (FF) of the MC14559B is used for register shortening where End of Conversion (EOC) is required after less than eight cycles. Applications for the MC14549B and MC14559B include analog-to-digital conversion, with serial and parallel outputs.

特長

- Totally Synchronous Operation
- All Outputs Buffered
- Single Supply Operation
- Serial Output
- Retriggerable
- Compatible with a Variety of Digital and Analog Systems such as the MC1408 8-Bit D/A Converter
- All Control Inputs Positive-Edge Triggered
- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- Capable of Driving 2 Low-Power TTL Loads, 1 Low-Power Schottky TTL Load or 2 HTL Loads Over the Rated Temperature Range
- Chip Complexity: 488 FETs or 122 Equivalent Gates

For more features, see the data sheet

電気的仕様

製品	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC14559BDWR2G	Pb-free Halide free	Active	Shift Register	1	3	18	420	2.25	SOIC-16W

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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