

製品概要

MC14081B: Quad 2-Input AND Gate

技術情報は、データシートをご参照ください。

The B Series logic gates are constructed with P and N channel enhancement mode devices in a single monolithic structure (Complementary MOS). Their primary use is where low power dissipation and/or high noise immunity is desired.

特長

- Supply Voltage Range = 3.0 Vdc to 18 Vdc
- All Outputs Buffered
- Capable of Driving Two Low-power TTL Loads or One Low-power Schottky TTL Load Over the Rated Temperature Range.
- Double Diode Protection on All Inputs Except: Triple Diode Protection on MC14011B and MC14081B
- Pin-for-Pin Replacements for Corresponding CD4000 Series B Suffix Devices
- Pb-Free Packages are Available*

電氣的仕様

製品	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC14081BDG	Pb-free	Active	AND	4	3	18	130	null	SOIC-14
	Halide free								
MC14081BDR2G	Pb-free	Active	AND	4	3	18	130	null	SOIC-14
	Halide free								
MC14081BDTR2G	Pb-free	Active	AND	4	3	18	130	null	TSSOP-14
	Halide free								
NLV14081BDG	AEC Qualified	Active	AND	4	3	18	null	null	SOIC-14
	PPAP Capable								
	Pb-free								
	Halide free								
NLV14081BDR2G	AEC Qualified	Active	AND	4	3	18	null	null	SOIC-14
	PPAP Capable								
	Pb-free								
	Halide free								
NLV14081BDTR2G	AEC Qualified	Active	AND	4	3	18	null	null	TSSOP-14
	PPAP Capable								
	Pb-free								
	Halide free								

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8/21/2019 作成