

製品概要

MC74AC14: Hex Inverter with Schmitt Trigger Input

技術情報は、データシートをご参照ください。

The MC74AC14/74ACT14 contains six logic inverters which accept standard CMOS Input signals (TTL levels for MC74ACT14) and provide standard CMOS output levels. They are capable of transforming slowly changing input signals into sharply defined, jitter-free output signals. In addition, they have a greater noise margin than conventional inverters. The MC74AC14/74ACT14 has hysteresis between the positive-going and negative-going input thresholds (typically 1.0 V) which is determined internally by transistor ratios and is essentially insensitive to temperature and supply voltage variations.

特長

- Schmitt Trigger Inputs
- Outputs Source/Sink 24 mA
- ACT14 Has TTL Compatible Inputs
- Pb-Free Packages are Available

電氣的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Type	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74AC14DG	0.1735	Pb-free Halide free	Active	Inverter	6	2	6	7	24	SOIC-14
MC74AC14DR2G	0.16	Pb-free Halide free	Active	Inverter	6	2	6	7	24	SOIC-14
MC74AC14DTR2G	0.1911	Pb-free Halide free	Active	Inverter	6	2	6	7	24	TSSOP-14
NLV74AC14DR2G	0.2	AEC Qualified PPAP Capable Pb-free Halide free	Active	Inverter	6	2	6	7	24	SOIC-14
NLV74AC14DTR2G	0.1911	AEC Qualified PPAP Capable Pb-free Halide free	Active	Inverter	6	2	6	7	24	TSSOP-14
NLVACT14DR2G	0.2787	AEC Qualified Pb-free Halide free	Active	Inverter	6	4.5	5.5	7	24	SOIC-14

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