

製品概要

MC74AC138: 1-of-8 Decoder/Demultiplexer

技術情報は、データシートをご参照ください。

The MC74AC138/74ACT138 is a high-speed 1-of-8 decoder/demultiplexer. This device is ideally suited for high-speed bipolar memory chip select address decoding. The multiple input enables allow parallel expansion to a 1-of-24 decoder using just three MC74AC138/74ACT138 devices or a 1-of-32 decoder using four MC74AC138/ 74ACT138 devices and one inverter.

特長

- Demultiplexing Capability
- Multiple Input Enable for Easy Expansion
- Active LOW Mutually Exclusive Outputs
- Outputs Source/Sink 24 mA
- ACT138 Has TTL Compatible Inputs
- These devices are available in Pb-free package(s). Specifications herein apply to both standard and Pb-free devices. Please see our website at www.onsemi.com for specific Pb-free orderable part numbers, or contact your local ON Semiconductor sales office or representative.

電気的仕様

製品	Compliance	Status	Channels	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
MC74AC138DG	Pb-free Halide free	Active	1	2	6	9.5	24	SOIC-16
MC74AC138DR2G	Pb-free Halide free	Active	1	2	6	9.5	24	SOIC-16
MC74AC138DTR2G	Pb-free Halide free	Active	1	2	6	9.5	24	TSSOP-16

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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