



NB3V63143G

1.8 V OmniClock (LVCMOS) (LVDS/HCSL) VDDO

OmniClock NB3V63143G (OTP) PLL 8 kHz 200 MHz (LVCMOS) 3 (LVCMOS) 1 1 (LVPECL/HCSL) (EMI) PLL () / OE[2:0] VDDO[2:0] 4 2 (SEL [1:0]) PLL Power Down (PD#) ()

- Member of the OmniClock Family of Programmable Clock Generators
 - Operating Power Supply: 1.8 V ± 0.1 V
 - I/O Standards - Inputs: LVCMOS, Fundamental Mode Crystal - Outputs: 1.8 V LVCMOS - Outputs: LVDS and HCSL
 - 3 Programmable Single Ended (LVCMOS) Outputs from 8 kHz to 200 MHz
 - 1 Programmable Differential Clock Output up to 200 MHz
 - Input Frequency Range - Crystal: 3 MHz to 50 MHz - Reference Clock: 3 MHz to 200 MHz
 - Configurable Spread Spectrum Frequency Modulation Parameters (Type, Deviation, Rate)
 - Individual Output Enable Pins
 - Independent Output Voltage Pins
 - Programmable Internal Crystal Load Capacitors
- For more features, see the data sheet

- Consumer
- Industrial Equipment
- Computing & Peripherals
- Portable equipment
- Smart Wearables
- Smart Phones
- Digital Cameras
- Camcorders
- Set Top Boxes

	Pricing (\$/Unit)	Compliance	Status	Input Level	Output Level	V _S Typ (V)	f _{in} Typ (MHz)	f _{out} Typ (MHz)	t _{jitter} (Cy-Cy) Typ (ps)	t _{jitter} (Period) Typ (ps)	t _{jitter} (Φ) Typ (ps)	t _R & t _F Typ (ps)	t _R & t _F Max (ps)	T _A Min (°C)	T _A Max (°C)	Package Type
NB3V63143G00 MNR2G	2.0125	 	Active			1.8		0.008 to 200	100	100		1000	700	-40	85	QFN-16