







NB3N1900K

PCIe 3.3 V 100/133 MHz 1

NB3N1900K PCIe Intel® QuickPath (Intel QPI & UPI) PCIe Gen1 Gen2 Gen3 Gen4 NB3N1900K PLL 100 MHz 133 MHz NB3N1900K HCSL

- Fixed Feedback Path for Lowest Input-to-Output Delay
 - Eight Dedicated OE# Pins for Hardware Control of Outputs
 - PLL Bypass Configurable for PLL or Fanout Operation
 - Selectable PLL Bandwidth
 - Spread Spectrum Compatible: Tracks Input Clock Spreading for Low EMI
 - SMBus Programmable Configurations
 - 100 MHz and 133 MHz PLL Mode to Meet the Next Generation PCIe Gen2 / Gen 3 / Gen 4 and Intel QPI & UPI Phase Jitter
 - 2 Tri-Level Addresses Selection (Nine SMBUS Addresses)
 - Cycle-to-Cycle Jitter: < 50 ps
 - Output-to-Output Skew: < 65 ps
- For more features, see the data sheet

- Industrial
- Networking
- Computing
- Consumer
- PCIe Gen 1, Gen 2, Gen 3, Gen 4
- Desktop
- Notebook
- Switchers/Routers
- Servers
- Set Top Box

	Pricing (\$/Unit)	Compliance	Status	Input Level	Output Level	Outputs Per Channel	V _{DD} Typ (V)	t _{skew(O-O)} Max (ps)	F Max (MHz)	t _{Jitter} Max (ps)	Package Type
NB3N1900KMN G	3.25	 	Active	HCSL	HCSL	19	3.3	85	133.33	50	QFN-72
NB3N1900KMN TWG	3.25	 	Active	HCSL	HCSL	19	3.3	85	133.33	50	QFN-72
NB3N1900KMN TXG	3.25	 	Active	HCSL	HCSL	19	3.3	85	133.33	50	QFN-72