



CAT24C512C8UTR

512 Kb CMOS EEPROM 1 8 65,536

CAT24C512C8UTR 512 Kb CMOS EEPROM 1 8 65,536 128 (100 kHz)Fast (400 kHz) Fast-Plus (1 MHz) I2C WP High ()
 CAT24C512C8UTR 8 On-Chip ECC (Error Correction Code)

- Supports Standard, Fast and Fast-Plus I2C Protocol
 - 1.8 V to 5.5 V Supply Voltage Range
 - 128-Byte Page Write Buffer
 - Hardware Write Protection for Entire Memory
 - Schmitt Triggers and Noise Suppression Filters on I2C Bus Inputs (SCL and SDA)
 - Low Power CMOS Technology
 - 1,000,000 Program/Erase Cycles
 - 100 Year Data Retention
 - Industrial and Extended Temperature Range
 - These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant
- For more features, see the data sheet

- Wireless devices - Cell phones (Smartphone)
 - Smartphone
 - Wireless devices

| | Pricing (\$/Unit) | Compliance | Status | Type | Density | Organization | Data Transmission Standard | f _{cycle} Max (kHz) | t _{ACC} Max ns | V _{CC} Min (V) | V _{CC} Max (V) | I _{stand by} Max (μA) | I _{act} Max (mA) | T Min (°C) | T Max (°C) | Package Type |
|----------------|-------------------|---|--------|------|---------|--------------|----------------------------|------------------------------|-------------------------|-------------------------|-------------------------|--------------------------------|---------------------------|------------|------------|--------------|
| CAT24C512C8UTR | 0.3786 |   | Active | | | | | | | | | | | | | WLC SP-8 |