

MC74VHC1GT50

TTL

MC74VHC1GT50CMOSCMOS3CMOS1.65 V5.5V CMOSMC74VHC1GT505.5 VVCC = 0V-

- High Speed: tPD = 3.5ns (Typ) at VCC = 5V
- Low Power Dissipation: ICC = 1µA (Max) at TA = 25°C
- Low threshold Inputs, TTL compatible if Vcc= 5 V: VIL = 0.8V; VIH = 2.0V
- CMOS-Compatible Outputs: VOH > 0.8VCC ; VOL CC @Load
- Power Down Protection Provided on Inputs and Outputs
- Balanced Propagation Delays
- Pin and Function Compatible with Other Standard Logic Families
- Chip Complexity: FETs = 3/4 100
- Pb-Free Packages are Available

	Pricing (\$/Unit)	Compliance	Status	Channels	Output	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _O Max (mA)	Package Type
M74VHC1GT50 DFT1G	0.0467		Active	1	CMOS	2	5.5	7.5	8	SC-88A / SC-70-5
M74VHC1GT50 DFT2G	0.0467		Active	1	CMOS	2	5.5	7.5	8	SC-88A / SC-70-5
M74VHC1GT50 DTT1G	0.0667		Active	1	CMOS	2	5.5	7.5	8	TSOP-5 / SOT-23-5
MC74VHC1GT5 ODBVT1G	0.0227		Active	1	CMOS	2	5.5	7.5	8	SC-74A
MC74VHC1GT5 0MU1TCG	0.1013		Active	1	CMOS	2	5.5	7.5	8	UDFN-6
MC74VHC1GT5 0MU2TCG			Product Preview	1	CMOS	2	5.5	7.5	8	UDFN-6
MC74VHC1GT5 0XV5T2G	0.06		Active	1	CMOS	2	5.5	7.5	8	SOT-553
NLV74VHC1GT5 0DTT1G	0.132		Active	1	CMOS	2	5.5	7.5	8	TSOP-5 / SOT-23-5
NLVVHC1GT50 DFT1G	0.1333		Active	1	CMOS	2	5.5	7.5	8	SC-88A / SC-70-5
NLVVHC1GT50 DFT2G			Active	1	CMOS	2	5.5	7.5	8	SC-88A / SC-70-5