







PCA9655E

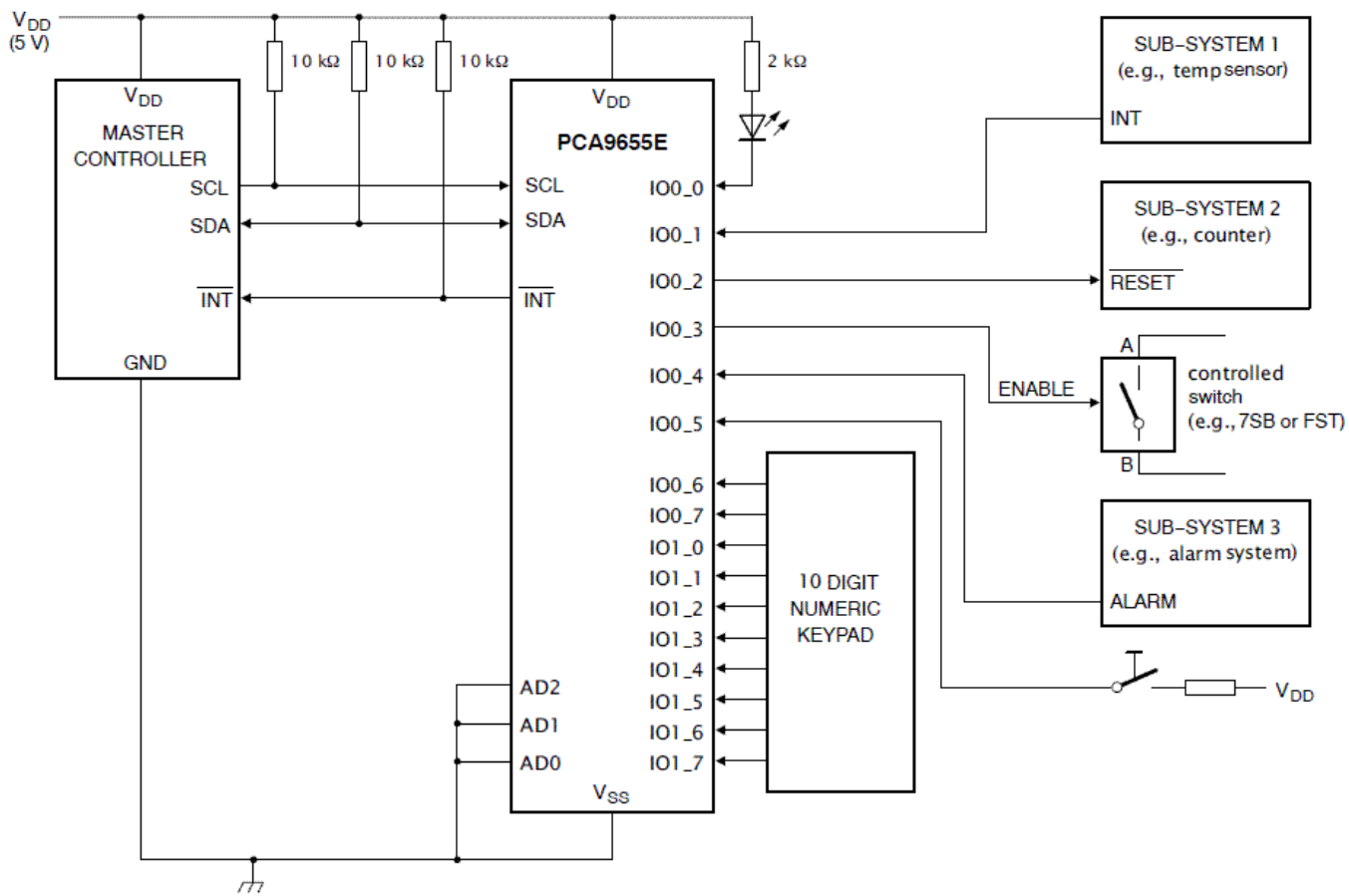
I/OI2C16

PCA9655E I2C-bus SMBus 16 (GPIO) PCA9655E 2 8 () (HIGH LOW) IO IO IO PCA9655E 3 (AD0AD1AD2) I2C-bus 64 I2C-bus SMBus

- VCC Operating Range: 1.65 V to 5.5 V
 - SDA Sink Capability: 30 mA
 - 5.5 V Tolerant I/Os
 - Polarity Inversion Register
 - Active LOW Interrupt Output
 - Low Standby Current
 - Noise Filter on SCL/SDA Inputs
 - No Glitch on Powerup
 - Internal Poweron Reset
 - 64 Programmable Slave Addresses Using Three Address Pins
- For more features, see the data sheet

- Input/output Port Expansion for Extended Addressing Capability
- Keyboards

	Pricing (\$/Unit)	Compliance	Status	I/O	Cascadable	V _{CC} Min (V)	V _{CC} Max (V)	Interrupt Output	I/O Pullups	LED Blink/PWM	I _o Min (mA)	Package Type
PCA9655EDTR2G	0.773	 	Active	16	8 Slave ID Addresses	1.65	5.5	Yes	Yes	No	25	TSSOP-24
PCA9655EDWR2G	0.773	 	Active	16	8 Slave ID Addresses	1.65	5.5	Yes	Yes	No	25	SOIC-24 WB
PCA9655EMTTXG	0.986	 	Active	16	8 Slave ID Addresses	1.65	5.5	Yes	Yes	No	25	QFN-24



Device address configured as 0100 000xb for this example.

IO0_0, IO0_2, IO0_3 configured as outputs.

IO0_1, IO0_4, IO0_5 configured as inputs.

IO0_6, IO0_7, and IO1_0 to IO1_7 configured as inputs.