







NLSX5014

4 100 Mbps 2

NLSX5014 4 IO Vcc IO VL 2 VCC VL VCC VL 0.9 V 4.5 V VL VCC NLSX5014 VCC VL VL VCC VL (VCC - 0.4) V NLSX5014 EMI NLSX5014 IO_VLn IO_VCCn (EN) EN I/O I/O

- Wide VCC, VL operating range: 0.9 V to 4.5 V
- VL and VCC are independent. VL may be greater than, equal to, or less than VCC
- High 100 pF capacitive drive capability
- High speed with 140 Mb/s guaranteed data rate for VCC, VL > 1.8 V
- Low bit to bit skew
- Overvoltage tolerant Enable and I/O pins
- Nonpreferential Power up sequencing
- Power Off protection
- Small packaging: 1.7 mm x 2.0 mm UQFN-12, SOIC-14, TSSOP-14
- These are PbFree devices
For more features, see the data sheet

- Mobile Phones, PDAs, Other Portable Devices

	Pricing (\$/Unit)	Compliance	Status	Channels	Input Level	V _{CC} Min (V)	V _{CC} Max (V)	t _{pd} Max (ns)	I _o Max (mA)	Package Type
NLSX5014DR2G	0.732	 	Active	4	CMOS	0.9	4.5	11	0.02	SOIC-14
NLSX5014DTR2G	0.5499	 	Active	4	CMOS	0.9	4.5	11	0.02	TSSOP-14
NLSX5014MUTA	0.4831	 	Active	4	CMOS	0.9	4.5	11	0.02	UQFN-12