

7.30

32X 0.40

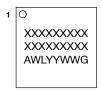
DIMENSIONS: MILLIMETERS

DATE 09 JUL 2015

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER
- DIMENSIONING AND TOLERANGING FER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. DIMENSION 6 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN
- 0.15 AND 0.25MM FROM THE TERMINAL TIP.
 COPLANARITY APPLIES TO THE EXPOSED
 PAD AS WELL AS THE TERMINALS.

| | MILLIMETERS | | |
|-----|-------------|------|--|
| DIM | MIN | MAX | |
| Α | 0.80 | 0.90 | |
| A1 | 0.00 | 0.05 | |
| А3 | 0.20 REF | | |
| b | 0.25 | 0.35 | |
| D | 7.00 BSC | | |
| D2 | 4.60 | 4.80 | |
| E | 7.00 BSC | | |
| E2 | 4.60 | 4.80 | |
| е | 0.65 BSC | | |
| L | 0.45 | 0.65 | |
| L1 | 0.00 | 0.15 | |

GENERIC MARKING DIAGRAM*



= Assembly Location

WL = Wafer Lot

YY = Year

WW = Work Week = Pb-Free Package

*This information is generic. Please refer

to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " ■", may or may not be present.

| DOCUMENT NUMBER: | 98AON23217F | Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. | |
|------------------|-------------------------|---|-------------|
| DESCRIPTION: | QFN32 7X7, 0.65MM PITCH | 1 | PAGE 1 OF 1 |

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

0.65 **PITCH**

5.00