D

В

Ε

PIN ONE

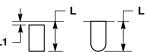
REFERENCE

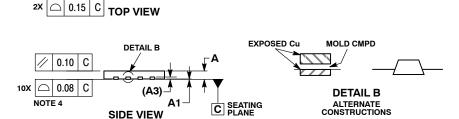
2X 🗀 0.15



UDFN8, 4x3, 0.8P CASE 506DC **ISSUE 0**

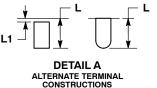
DATE 05 FEB 2015

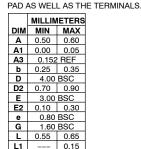




0.10 M C A B

0.05 M C NOTE 3





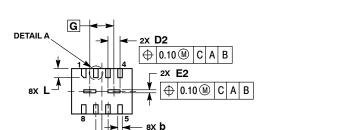
 DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.

DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN

0.15 AND 0.30MM FROM THE TERMINAL TIP. COPLANARITY APPLIES TO THE EXPOSED

NOTES:

3.



BOTTOM VIEW

e/2

GENERIC MARKING DIAGRAM*



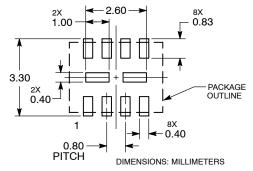
XXXXX = Specific Device Code Α = Assembly Location

Υ = Year WW = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	UDFN8, 4X3, 0.8P		PAGE 1 OF 1

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