

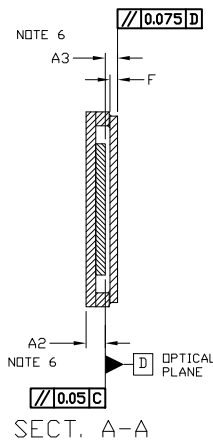
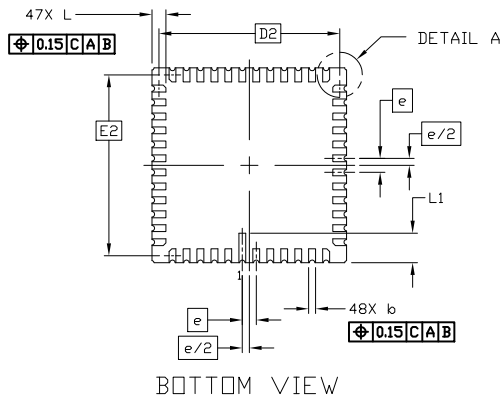
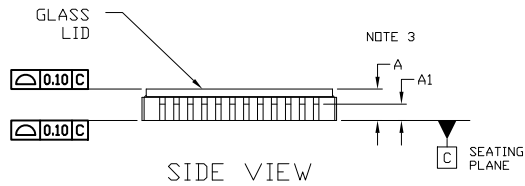
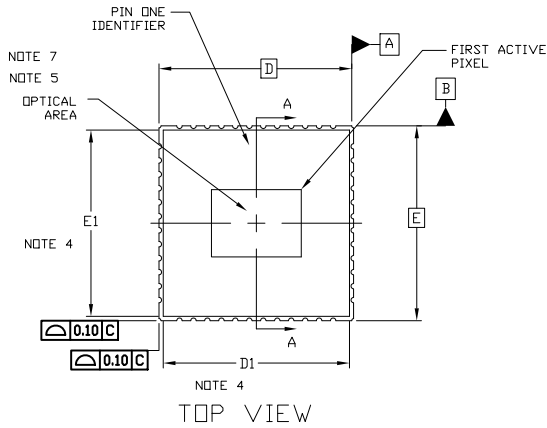
# MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

ON Semiconductor®



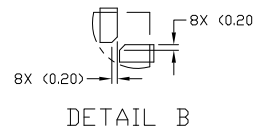
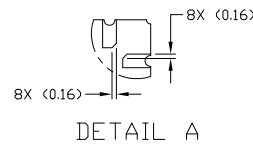
## CLCC48 14.22x14.22 CASE 848AR ISSUE A

DATE 07 AUG 2018

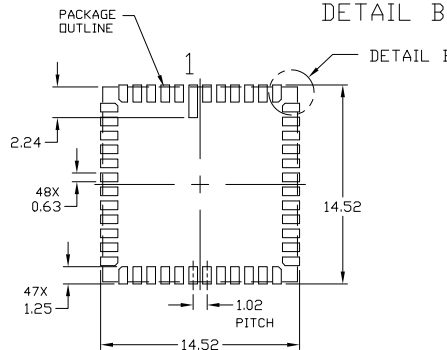


NOTES:

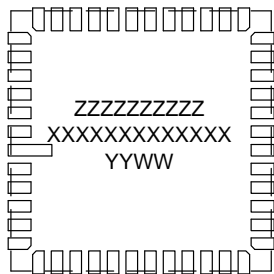
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION A INCLUDES THE PACKAGE BODY AND LID BUT DOES NOT INCLUDE HEATSINKS OR OTHER ATTACHED FEATURES.
4. THE LID DEFINED BY DIMENSIONS D1 AND E1 MUST BE LOCATED WITHIN DIMENSIONS D AND E.
5. MAXIMUM ROTATION OF OPTICAL AREA RELATIVE D AND E WILL BE 1°. OPTICAL AREA IS DEFINED BY THE ACTIVE PIXEL ARRAY. REFER TO THE DEVICE DATA SHEET FOR TOTAL ARRAY AND FIRST PIXEL DEFINITIONS.
6. PARALLELISM APPLIES ONLY TO THE OPTICAL AREA.
7. OPTICAL CENTER OFFSET WITH RESPECT TO THE PACKAGE CENTER IS X= 0.0 MICRONS, Y= 0.0 MICRONS ±100 MICRONS.



DIM	MILLIMETERS	
	MIN.	MAX.
A	---	2.50
A1	1.20	REF
A2	1.325	1.475
A3	0.800	1.000
b	0.46	0.56
D	14.22	BSC
D1	13.50	13.70
D2	13.20	BSC
E	14.22	BSC
E1	13.50	13.70
E2	13.20	BSC
e	1.02	BSC
F	0.50	0.60
L	0.82	1.22
L1	1.96	2.36



### GENERIC MARKING DIAGRAM\*



ZZZZ = Lot Code  
XXXX = Specific Device Code  
YY = Year  
WW = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	CLCC48 14.22x14.22	PAGE 1 OF 1

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