



Schematic for the AP1302CSSL00SMGAH-GEVB Evaluation Board

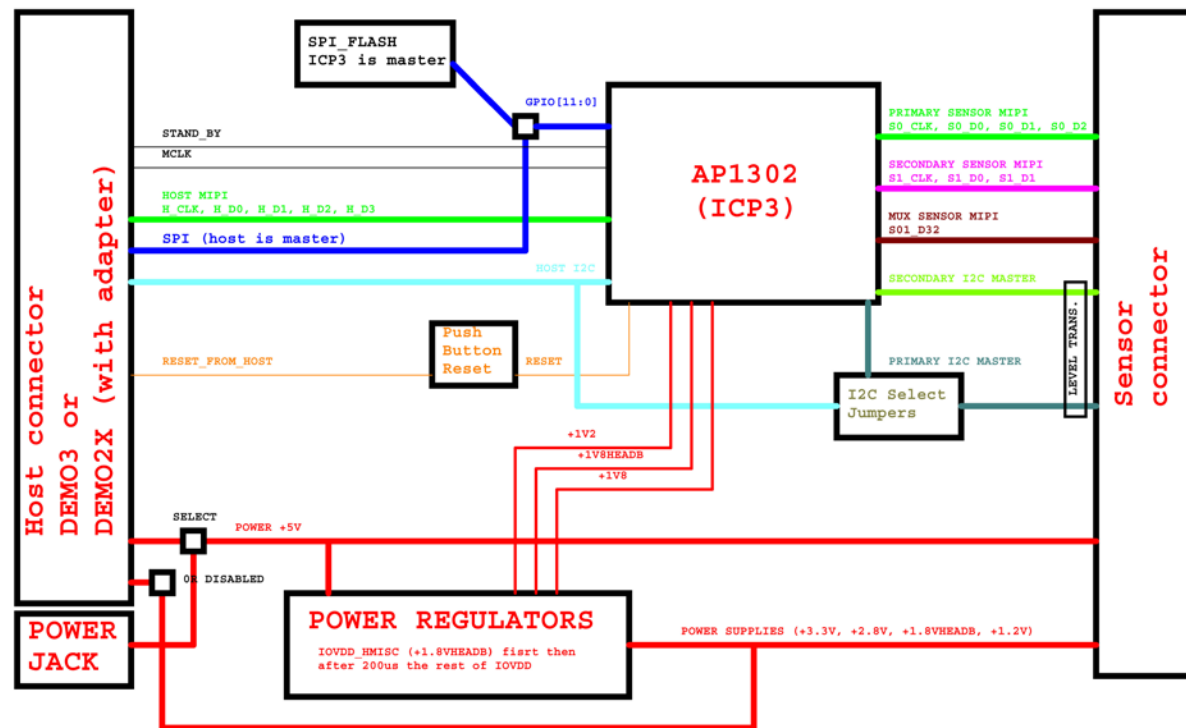
AP1302_ICP3_120VFBGA_DEMO

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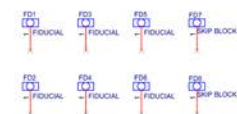
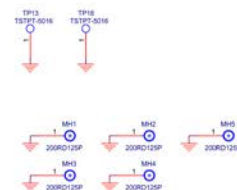
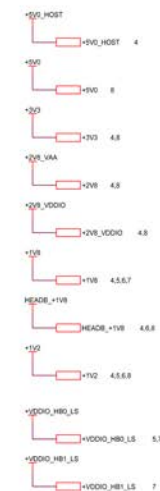
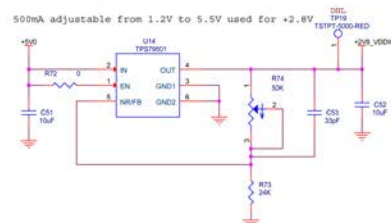
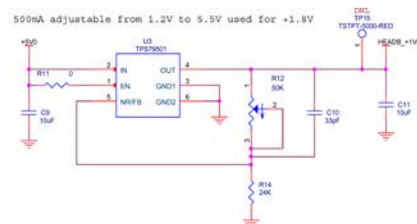
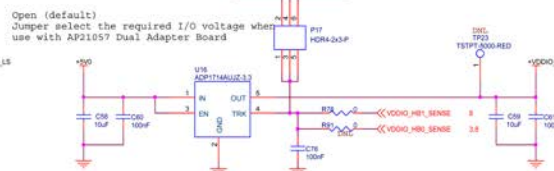
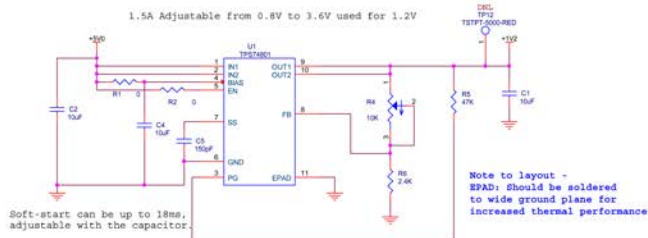
Rev	Who	Date	Description
Rev 0.0	Joe KK	02/14/12	Initial - Derive from AP1300_ICP2_Demo_Rev1 design



Block Diagram

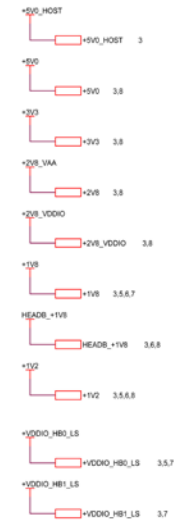
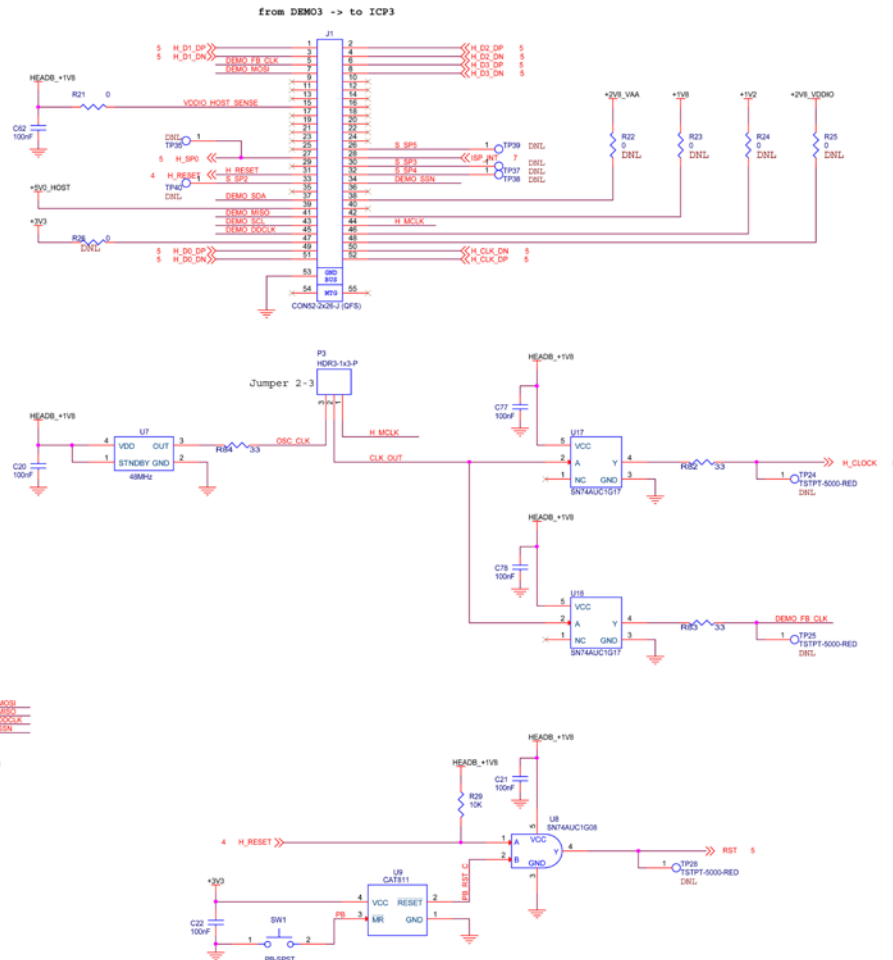
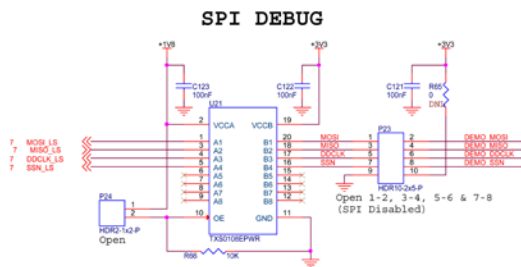
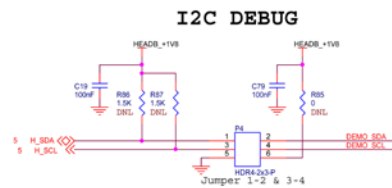


Power Supplies

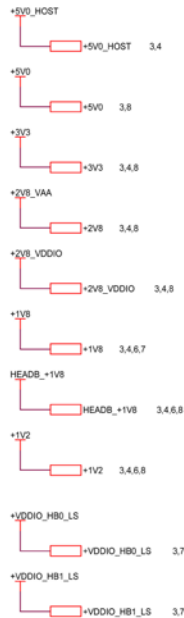




Connector to Host (Demo3 Baseboard)

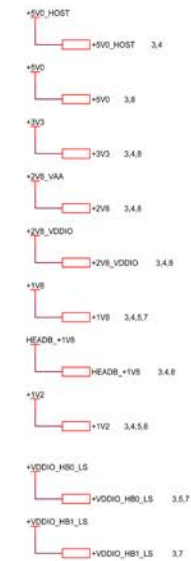
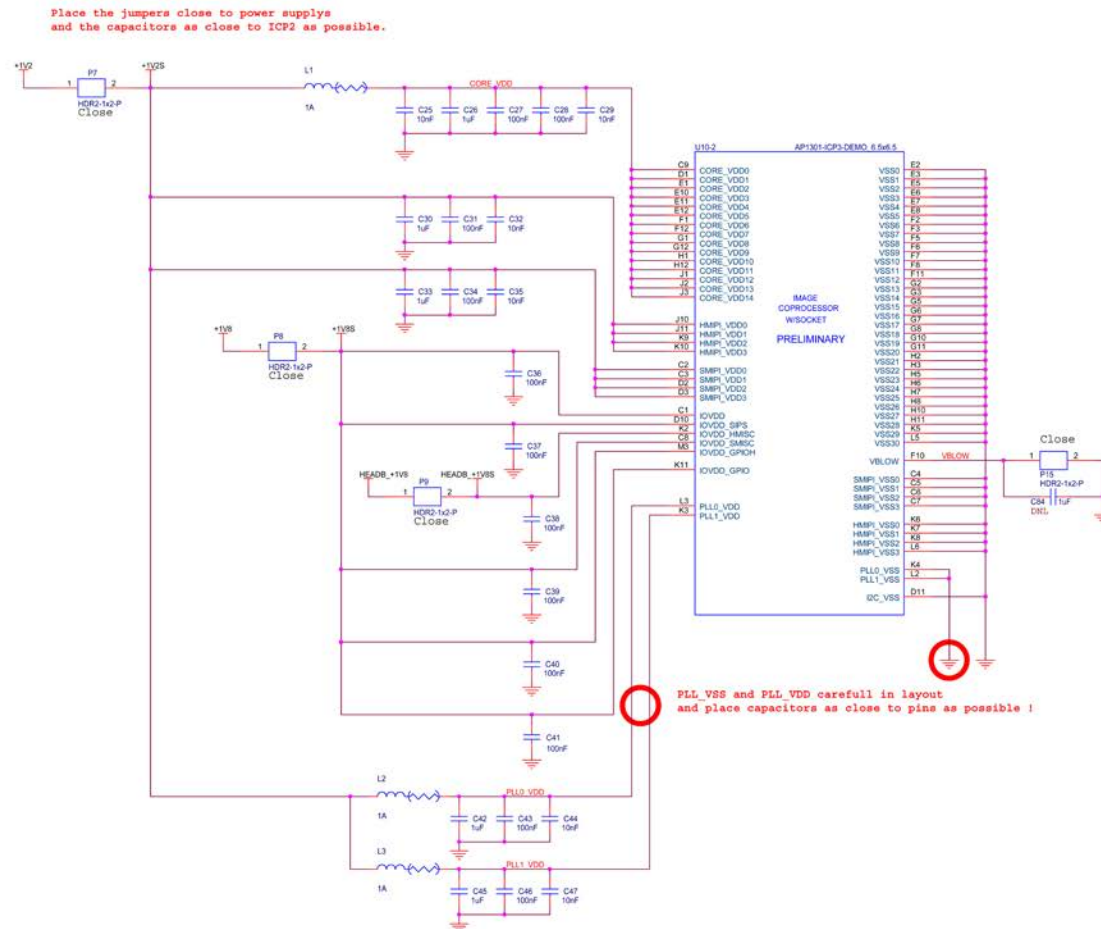


AP1302 Signals





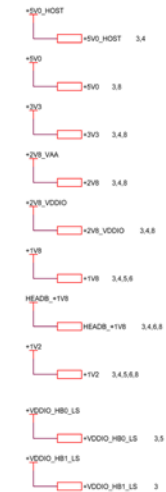
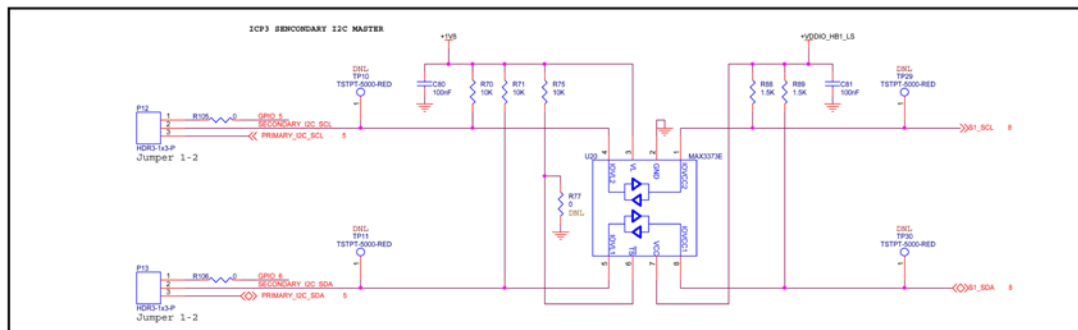
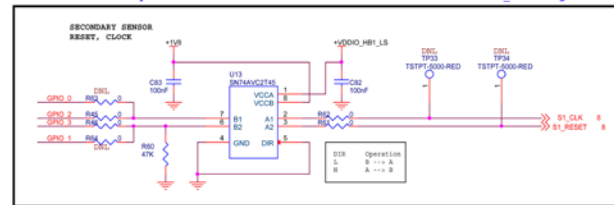
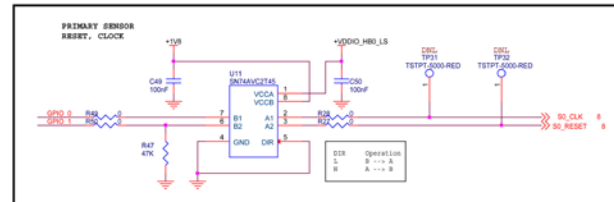
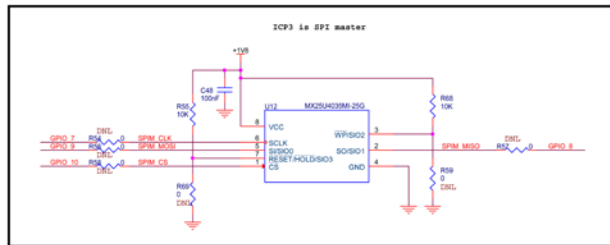
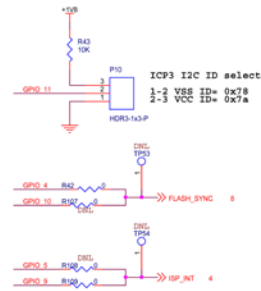
AP1302 Powers





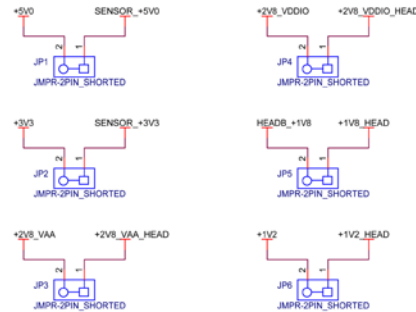
AP1302 GPIOs

GPIO	FUNCTION	RESET VALUE
0	SEN_CLK1	HIGH-Z
1	SHUTDOWN1	0
2	SEN_CLK2	HIGH-Z
3	SHUTDOWN2 / SPIS_CLK	1
4	SPIS_MISO	HIGH-Z
5	SPIS_MOSI / 2ND_I2C_SCL	0
6	SPIS_CS / 2ND_I2C_SDA	1
7	SPIM_CLK	HIGH-Z
8	SPIM_MISO	HIGH-Z
9	SPIM_MOSI	0
10	SPIM_CS	1
11	ICP3 I2C ID SELECT	HIGH-Z

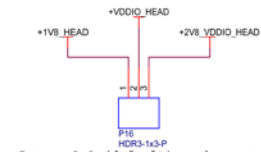




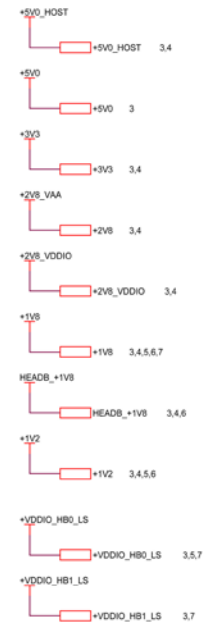
Connector to Sensors (Demo3 Headboards)



Power Debug Header: Cut away the shorted trace and mount header for power debugging



Jumper 2-3 (default) - when use with Demo3 Headboard
Jumper 1-2 - when use with AP21057 Dual Adapter Board



from ICP3 --> to Headboard or Dual Adapter Board

