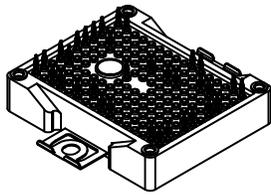


MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

ON Semiconductor®

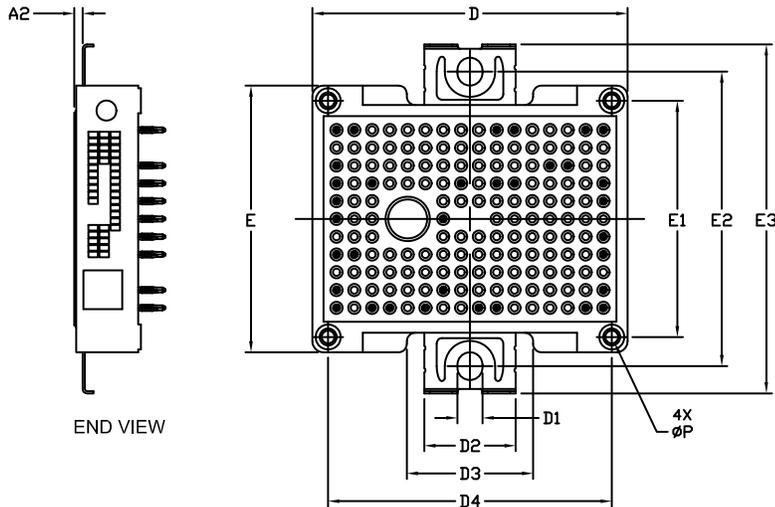


PIM34, 56.7x42.5 (PRESSFIT)
CASE MODCR
ISSUE A

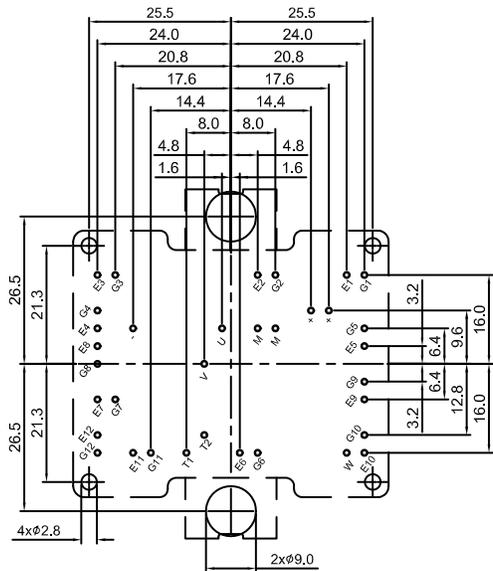
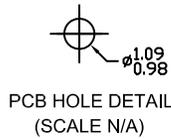
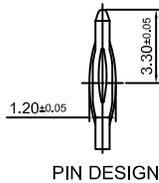
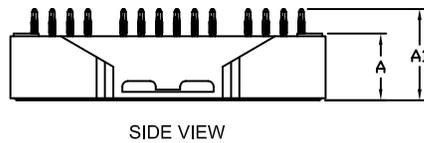
DATE 11 SEP 2018

NOTES:

1. CONTROLLING DIMENSION: MILLIMETERS



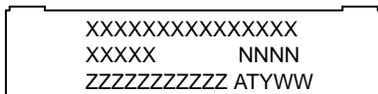
DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	11.65	12.00	12.35
A1	15.90	16.40	16.90
A2	1.20	1.40	1.60
D	56.40	56.70	57.00
D1	4.40	4.50	4.60
D2	16.20	16.40	16.60
D3	22.40	22.70	23.00
D4	50.85	51.00	51.15
E	42.35	42.50	42.65
E1	47.70	48.00	48.30
E2	52.90	53.00	53.10
E3	62.30	62.80	63.30
P	2.20	2.30	2.40



PCB HOLE PATTERN

(View from PCB Top Layer downward to backside of PCB Layer)

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
 NNN = Serial Number
 ZZZ = Lot ID
 AT = Assembly & Test Location
 Y = Year
 W = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON13517G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	PIM34, 56.7x42.5 (PRESSFIT)	PAGE 1 OF 1

ON Semiconductor and ON are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.