N-Channel Power MOSFET 600 V, 3.6 Ω

Features

- Low ON Resistance
- Low Gate Charge
- ESD Diode-Protected Gate
- 100% Avalanche Tested
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



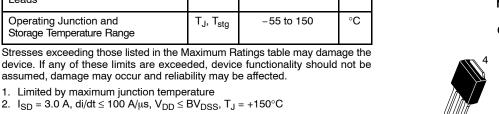
ON Semiconductor®

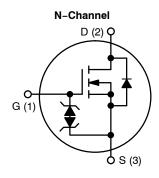
www.onsemi.com

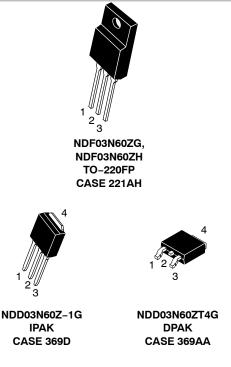
V _{DSS}	R _{DS(on)} (MAX) @ 1.2 A
600 V	3.6 Ω

Rating	Symbol	NDF	NDD	Unit
Drain-to-Source Voltage	V _{DSS}	600		V
Continuous Drain Current $R_{\theta JC}$	۱ _D	3.1 (Note 1)	2.6	A
Continuous Drain Current $R_{\theta JC}$ T_A = 100°C	۱ _D	2.9 (Note 1)	1.65	A
Pulsed Drain Current, $V_{GS} \mathbin{@} 10 \ V$	I _{DM}	12	10	А
Power Dissipation $R_{\theta JC}$	PD	27	61	W
Gate-to-Source Voltage	V _{GS}	±30		V
Single Pulse Avalanche Energy, $I_D = 3.0 A$	E _{AS}	100		mJ
ESD (HBM) (JESD 22-A114)	V _{esd}	3000		V
RMS Isolation Voltage (t = 0.3 sec., R.H. \leq 30%, T_A = 25°C) (Figure 17)	V _{ISO}	4500		V
Peak Diode Recovery (Note 2)	dv/dt	4.5		V/ns
Continuous Source Current (Body Diode)	۱ _S	3.0		A
Maximum Temperature for Soldering Leads	ΤL	260		°C
Operating Junction and Storage Temperature Range	T _J , T _{stg}	–55 to 150		°C

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)







MARKING AND ORDERING INFORMATION

See detailed ordering and shipping information on page 7 of this data sheet.

THERMAL RESISTANCE

Parameter			Value	Unit
Junction-to-Case (Drain)	NDF03N60Z NDD03N60Z	$R_{\theta JC}$	4.7 2.0	°C/W
Junction-to-Ambient Steady State	(Note 3) NDF03N60Z (Note 4) NDD03N60Z (Note 3) NDD03N60Z-1	$R_{ heta JA}$	51 40 80	

3. Insertion mounted

4. Surface mounted on FR4 board using 1" sq. pad size, (Cu area = 1.127 in sq [2 oz] including traces).

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

Characteristic	Test Conditions		Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	$V_{GS} = 0 V, I_D = 1 mA$		BV _{DSS}	600			V
Breakdown Voltage Temperature Co- efficient	Reference to 25°C, I _D = 1 mA		$\Delta BV_{DSS}/\Delta T_{J}$		0.6		V/°C
Drain-to-Source Leakage Current	25°C		I _{DSS}			1	μΑ
	$V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V}$ 150°C				50		
Gate-to-Source Forward Leakage	V _{GS} = ±20 V		I _{GSS}			±10	μΑ
ON CHARACTERISTICS (Note 5)							
Static Drain-to-Source On-Resistance	V_{GS} = 10 V, I _D = 1.2 A		R _{DS(on)}		3.3	3.6	Ω
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 50 \ \mu A$		V _{GS(th)}	3.0	3.9	4.5	V
Forward Transconductance	V _{DS} = 15 V, I _D = 1.5 A		9 FS		2.0		S
DYNAMIC CHARACTERISTICS							
Input Capacitance (Note 6)	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz		C _{iss}	248	312	372	pF
Output Capacitance (Note 6)			C _{oss}	30	39	50	
Reverse Transfer Capacitance (Note 6)			C _{rss}	4	8	12	
Total Gate Charge (Note 6)	V _{DD} = 300 V, I _D = 3.0 A, V _{GS} = 10 V		Qg	6	12	18	nC
Gate-to-Source Charge (Note 6)			Q _{gs}	1.5	2.5	4	
Gate-to-Drain ("Miller") Charge (Note 6)			Q _{gd}	3	6.1	9	
Plateau Voltage			V _{GP}		6.4		V
Gate Resistance			Rg		6.0		Ω
RESISTIVE SWITCHING CHARACTER	STICS				•		•
Turn-On Delay Time	V_{DD} = 300 V, I _D = 3.0 A, V _{GS} = 10 V, R _G = 5 Ω		t _{d(on)}		9		ns
Rise Time			t _r		8		
Turn-Off Delay Time			t _{d(off)}		16		
Fall Time			t _f		10		
SOURCE-DRAIN DIODE CHARACTER	ISTICS (T _C = 25°C unless otherw	vise note	ed)		-	-	-
Diode Forward Voltage	I _S = 3.0 A, V _{GS} = 0 V		V _{SD}			1.6	V
Reverse Recovery Time	V _{GS} = 0 V, V _{DD} = 30 V		t _{rr}		265		ns
	$v_{GS} = 0 v, v_{DD} = 30 v$						

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

 $\begin{array}{l} V_{GS} = 0 \ V, \ V_{DD} = 30 \ V \\ I_S = 3.0 \ A, \ di/dt = 100 \ A/\mu s \end{array}$

Q_{rr}

μC

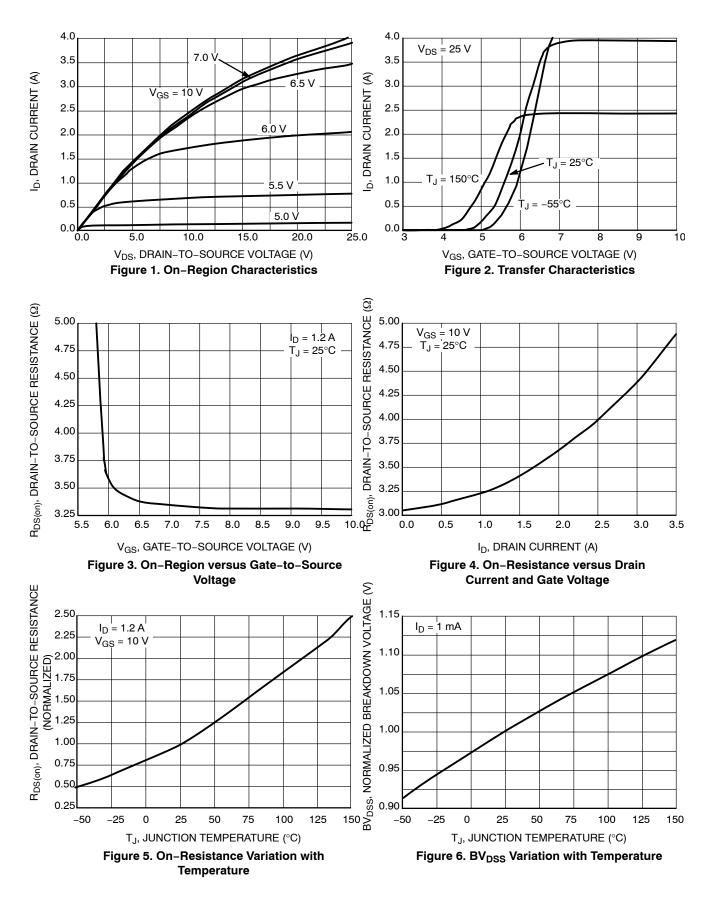
0.9

5. Pulse Width \leq 380 µs, Duty Cycle \leq 2%.

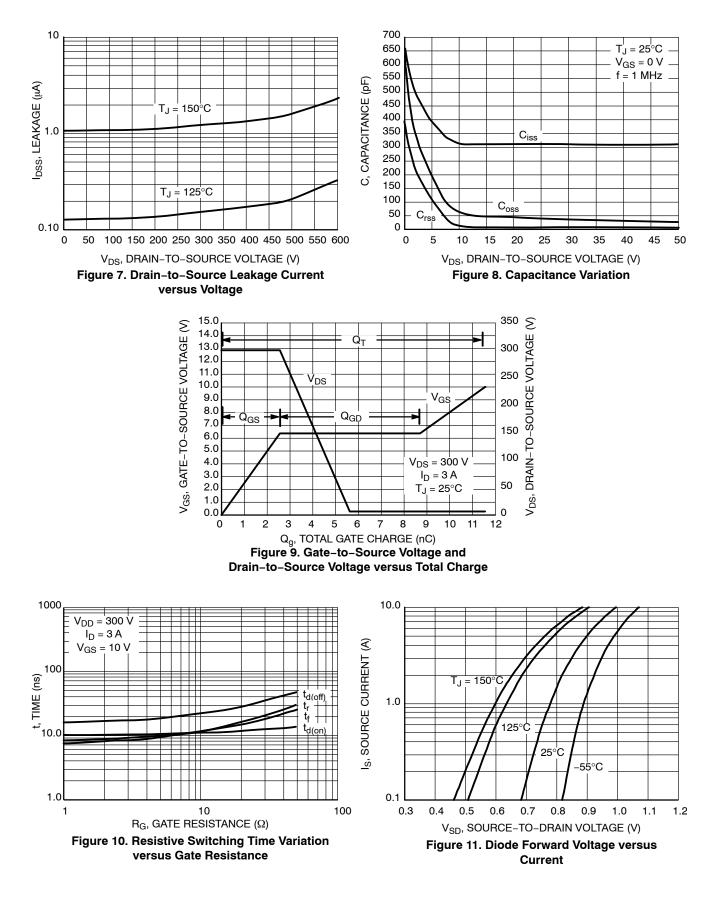
6. Guaranteed by design.

Reverse Recovery Charge

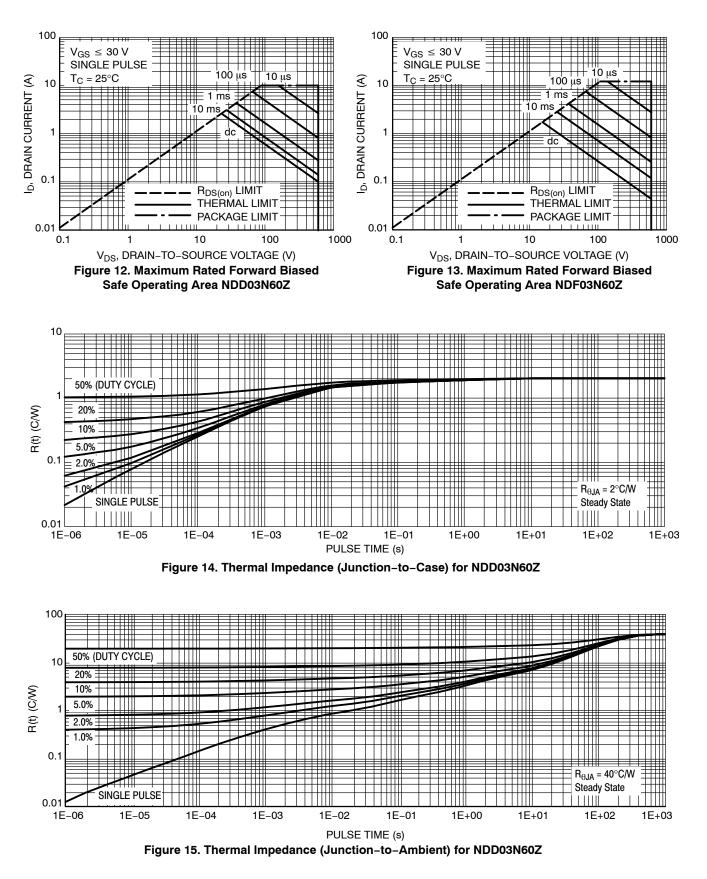
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



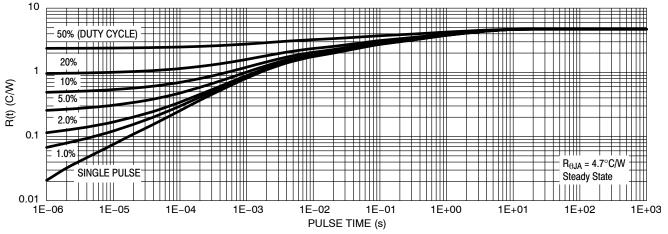


Figure 16. Thermal Impedance (Junction-to-Case) for NDF03N60Z

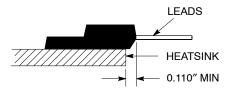
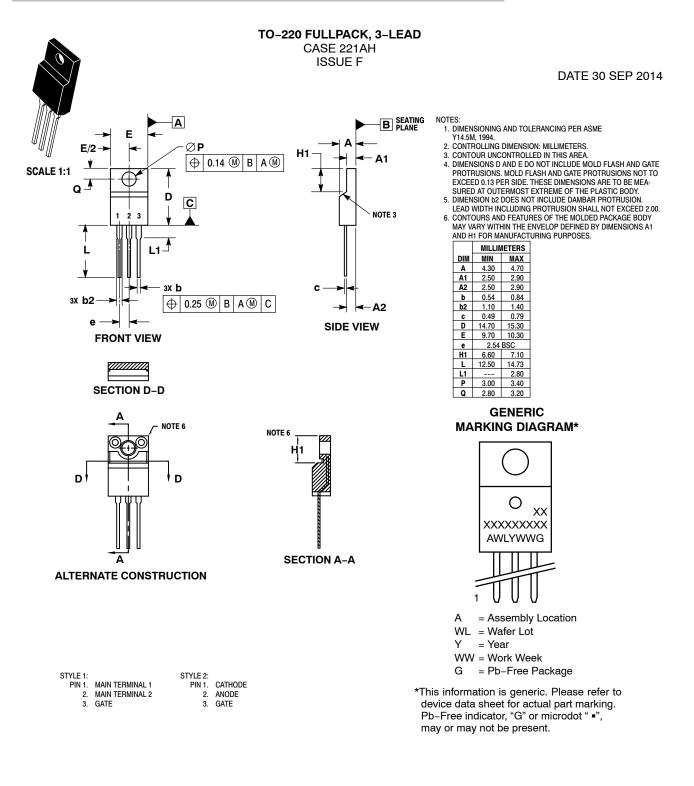


Figure 17. Isolation Test Diagram

Measurement made between leads and heatsink with all leads shorted together.

*For additional mounting information, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

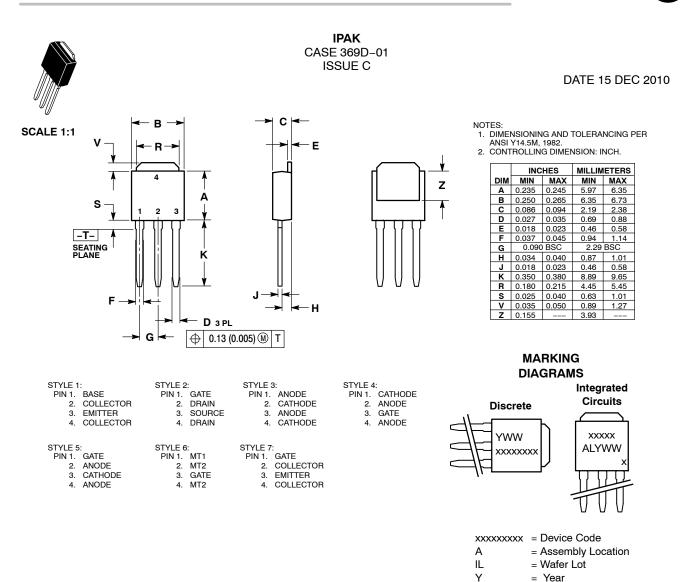




DOCUMENT NUMBER:	98AON52577E	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	TO-220 FULLPACK, 3-LEAD		PAGE 1 OF 1		
ON Semiconductor and use trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the					

rights of others.

ON



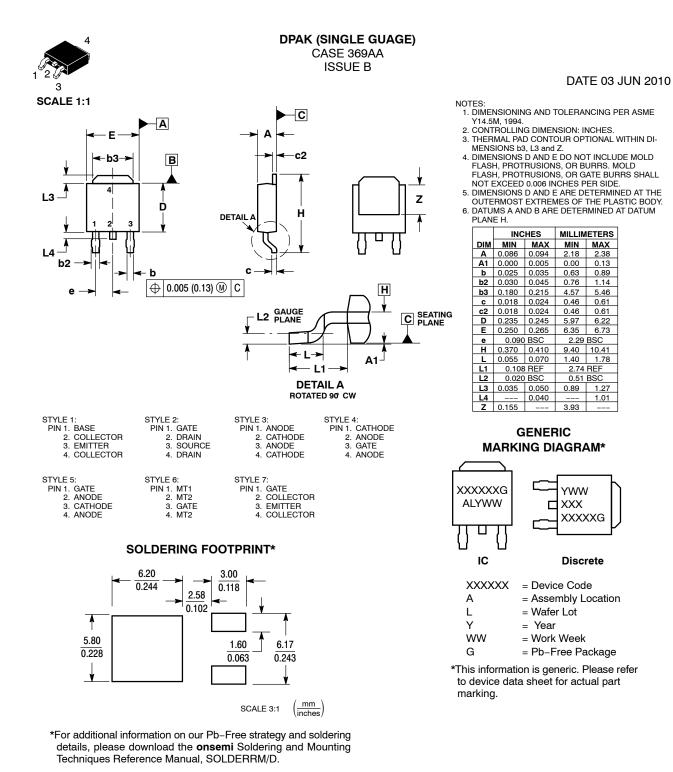
DOCUMENT NUMBER:	98AON10528D	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	IPAK (DPAK INSERTION MOUNT)		PAGE 1 OF 1		
ON Semiconductor and use trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product rights nor the disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the					

WW

= Work Week

rights of others.

onsemi



 DOCUMENT NUMBER:
 98AON13126D
 Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.

 DESCRIPTION:
 DPAK (SINGLE GAUGE)
 PAGE 1 OF 1

onsemi and ONSEMi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights of others.

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent_Marking.pdf</u>. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or indental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification. Buyer shall indemnify and hold onsemi and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs,

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation onsemi Website: www.onsemi.com

ONLINE SUPPORT: <u>www.onsemi.com/support</u> For additional information, please contact your local Sales Representative at <u>www.onsemi.com/support/sales</u>