SD05T1 Series, SZSD05T1 Series

ESD Protrection Diode

SOD-323 Diodes for ESD Protection

These surge protection diodes are designed for applications requiring transient overvoltage protection capability. They are intended for use in voltage and ESD sensitive equipment such as computers, printers, business machines, communication systems, medical equipment and other applications. These devices are ideal for situations where board space is at a premium.

Specification Features:

- Steady State Power Routing of 300 mW
- Peak Power 350 W (8 \times 20 μ s)
- Low Leakage
- Cathode Indicated by Polarity Band
- Package Weight: 4.507 mg/wmt
- Meets IEC61000-4-2 Level 4, 15 kV (Air), 8 kV (Contact)
- Meets IEC6100-4-4 Level 4, 40 A
- Meets IEC6100-4-5 (Lightning), 24 A
- Meets 16 kV Human Body Model ESD Requirements
- SZ Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic

Epoxy Meets UL 94, V-0

LEAD FINISH: 100% Matte Sn (Tin)

MOUNTING POSITION: Any

QUALIFIED MAX REFLOW TEMPERATURE: 260°C

Device Meets MSL 1 Requirements

Use the Device Number to order the 7 inch/3,000 unit reel. Replace the "T1" with "T3" in the Device Number to order the 13 inch/10,000 unit reel.



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MARKING DIAGRAM



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SOD-323 CASE 477 STYLE 1

xx = Specific Device Code

ZA = SD05T1ZC = SD12T1

1 = Month Code

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
SD05T1G		
SZSD05T1G	SOD-323	2000/Tono 9 Bool
SD12T1G	(Pb-Free)	3000/Tape & Reel
SZSD12T1G		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

SD05T1 Series, SZSD05T1 Series

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Power Dissipation @ 20 μs (Note 1) @ $T_L \le 25^{\circ}C$	P _{pk}	350	Watts
IEC 61000-4-2 (ESD)	Air act	±15 ±8.0	kV
IEC 61000-4-4 (EFT)		40	Α
ESD Voltage (Human Body Model (HBM) Waveform per IEC 61000-4-2)	V _{PP}	30	kV
Total Power Dissipation on FR-4 Board (Note 2) @ T _A = 25°C Derate above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{ hetaJA}$	416	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	TL	260	°C

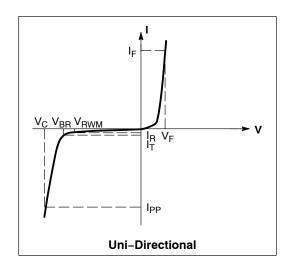
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
*Other voltages may be available upon request.

- Nonrepetitive current pulse, per Figure 6.
- 2. FR-4 printed circuit board, single-sided copper, mounting pad 1 cm².

ELECTRICAL CHARACTERISTICS

(T_A = 25°C unless otherwise noted)

Symbol	Parameter
I _{PP}	Maximum Reverse Peak Pulse Current
V _C	Clamping Voltage @ I _{PP}
V _{RWM}	Working Peak Reverse Voltage
I _R	Maximum Reverse Leakage Current @ V _{RWM}
V_{BR}	Breakdown Voltage @ I _T
I _T	Test Current
IF	Forward Current
V _F	Forward Voltage @ I _F



ELECTRICAL CHARACTERISTICS

			V _{BR} , Breakdown Voltage (V)			V _C @ I _{PP} = 5 A	Max I _{PP}	V _C @ Max I _{PP}	Max Capacitance (pF)
Device	V _{RWM} (V)	I _R @ V _{RWM} (μΑ)	Min	Max	I _T mA	(Note 3) (V)	(Note 3) (A)	(Note 3) (V)	V _R = 0 V f = 1.0 MHz
SD05T1G	5.0	10	6.2	7.3	1.0	9.8	24	14.5	350
SD12T1G	12	1.0	13.3	15.75	1.0	19	15	25	150

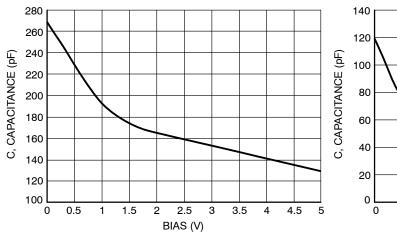
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

^{3.} $8 \times 20~\mu s$ pulse waveform.

^{*}Include SZ-prefix devices where applicable.

SD05T1 Series, SZSD05T1 Series

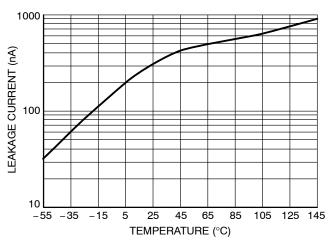
TYPICAL CHARACTERISTICS



140 120 100 80 60 40 20 0 2 4 6 8 10 12 BIAS (V)

Figure 1. SD05 Typical Capacitance versus Bias Voltage

Figure 2. SD12 Typical Capacitance versus Bias Voltage



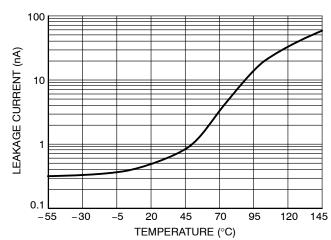
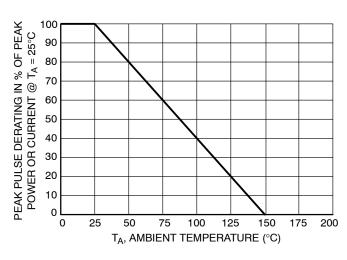


Figure 3. SD05 Typical Leakage Current versus Temperature

Figure 4. SD12 Typical Leakage Current versus Temperature



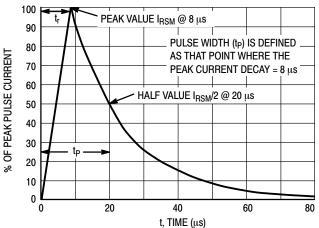


Figure 5. Pulse Derating Curve

Figure 6. 8 \times 20 μs Pulse Waveform

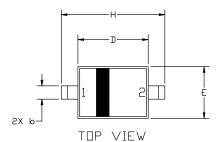






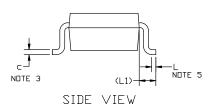
SOD-323 1.70x1.25x0.85 **CASE 477 ISSUE K**

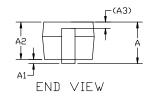
DATE 11 MAR 2024



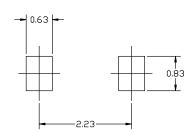
NOTES:

- 1. DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M, 2018.
- CONTROLLING DIMENSION: MILLIMETERS. LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH 3. SOLDER PLATING.
- DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
 DIMENSION L IS MEASURE FROM END OF RADIUS.





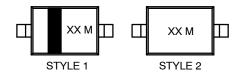
DIM	MILLIMETERS				
ואונע	MIN.	N□M. 0.90 0.05 0.85	MAX.		
Α	0.80	0.90	1.00		
A1	0.00	0.05	0.10		
A2	0.75	0.85	0.95		
А3	0.15 (REF)				
b	0.25	0.32	0.4		
C	0.09	0.12	0.18		
D	1.60	1.70	1.80		
Ε	1.15	1.25	1.35		
Н	2.30	2.50	2.70		
L	0.08				
L1	0.40 (REF)				



RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques
Reference manual, SDLDERRM/D.

GENERIC MARKING DIAGRAM*



XX = Specific Device Code M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

STYLE 2: NO POLARITY PIN 1. CATHODE (POLARITY BAND) 2. ANODE

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DESCRIPTION:	SOD-323 1.70x1.25x0.85		PAGE 1 OF 1		

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