

ArrayRDM-0112A20-GEVB Schematic

The ArrayRDM-0112A20-GEVB evaluation board schematic is shown below and consists of:

- ArrayRDM-0112A20-QFN SiPM array
- 12 U.FL connectors for access to each pixel cathode for signal readout
- An SMA connector for applying the bias to the common anode
- Bias filtering circuit
- Decoupling capacitors (12 x 10 nF and 4 x 100 nF decoupling capacitors from anode to ground – not shown)

Note that a negative bias supply should be supplied via the SMA connector (J13) and the U.FL connectors (J1 to J12) should be 50Ω terminated.



