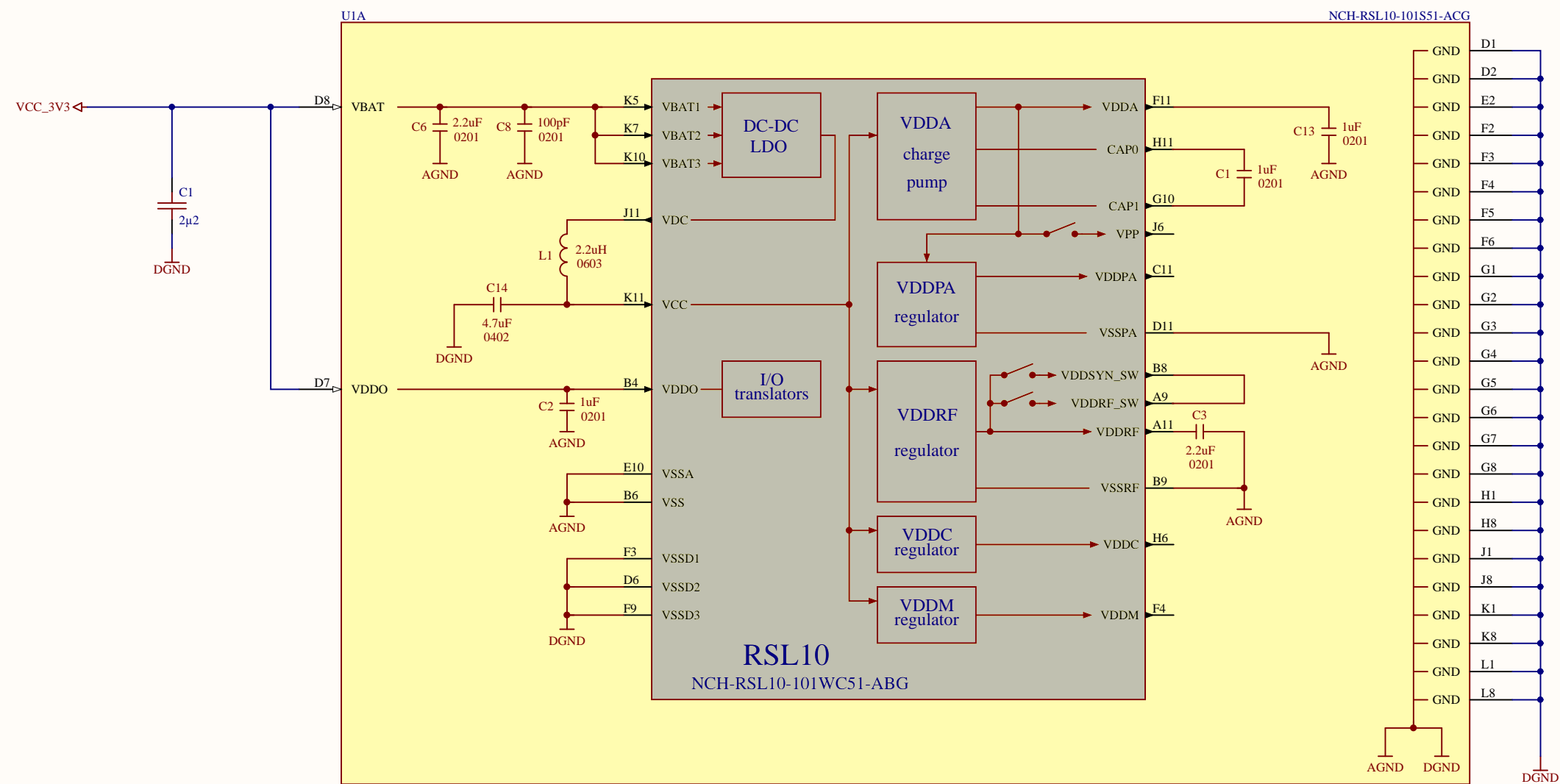

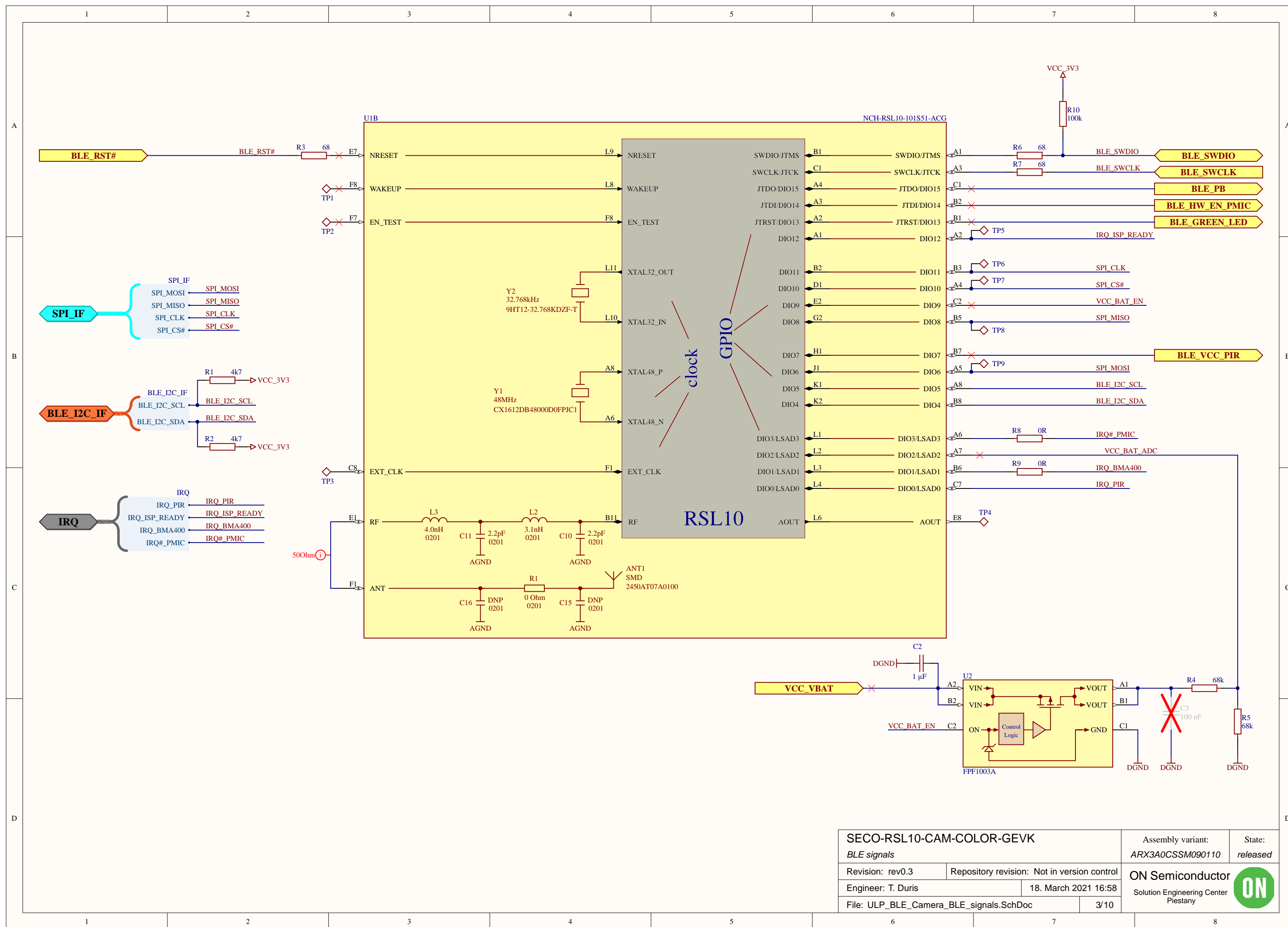


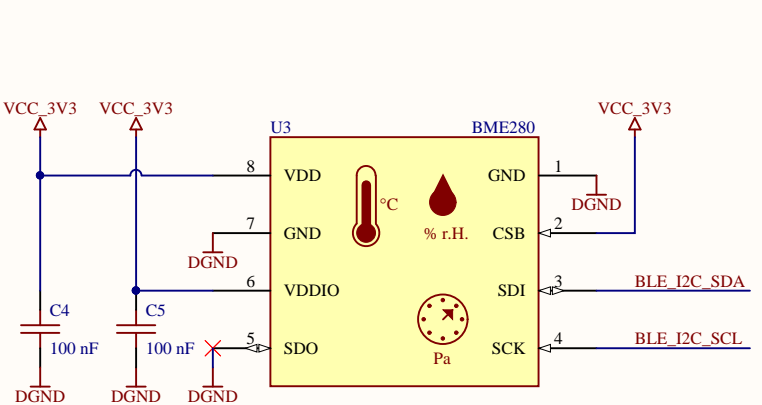
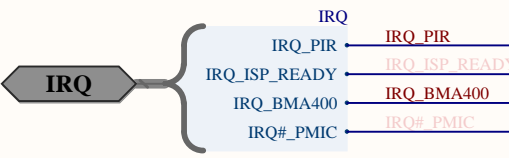
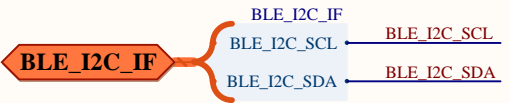
SECO-RSL10-CAM-COLOR-GEVK			Assembly variant:	State:
Block diagram			ARX3A0CSSM090110	released
Revision: rev0.3	Repository revision: Not in version control		ON Semiconductor Solution Engineering Center Piestany	
Engineer: T. Duris	18. March 2021 16:58			
File: ULP_BLE_Camera_block_diagram.SchDoc	1/10			



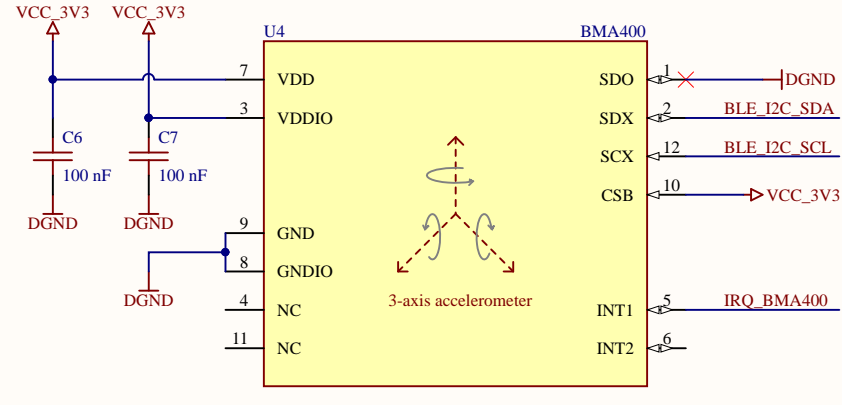


SECO-RSL10-CAM-COLOR-GEVK			Assembly variant:		State:	
BLE power			ARX3A0CSSM090110		released	
Revision: rev0.3		Repository revision: Not in version control		<div>ON Semiconductor</div> <div>Solution Engineering Center Piestany</div> <div></div>		
Engineer: T. Duris		18. March 2021 16:58				
File: ULP_BLE_Camera_BLE_power.SchDoc		2/10				

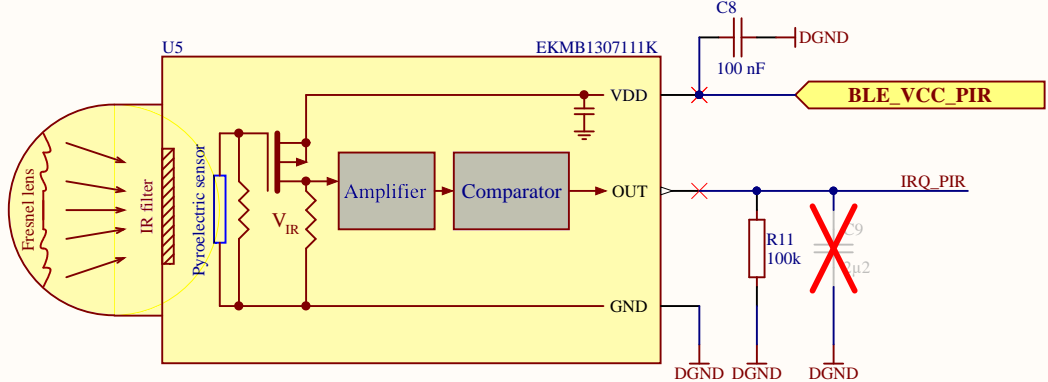





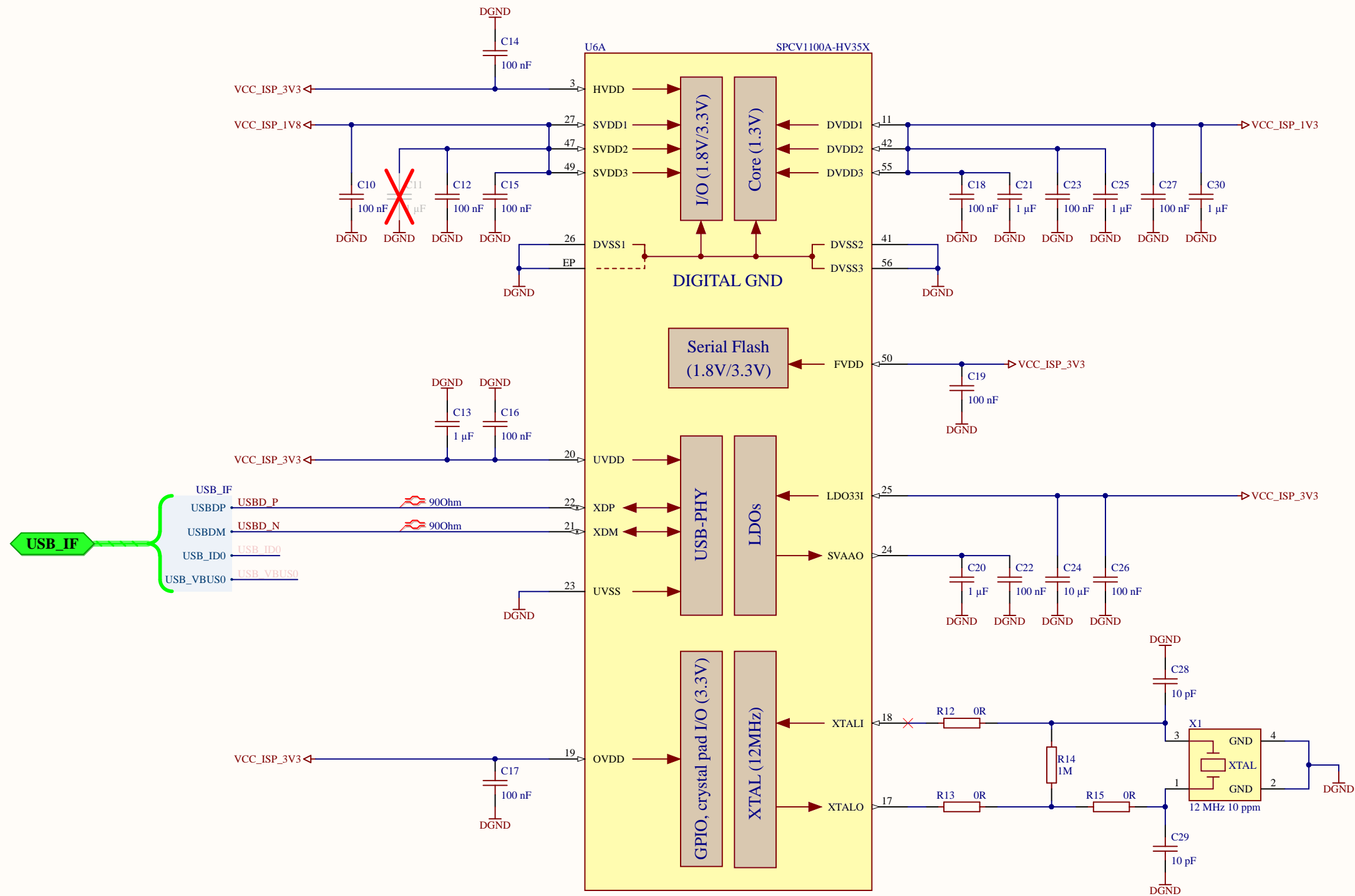
I2C slave address (7-bit): 0x76



I2C slave address (7-bit): 0x14



SECO-RSL10-CAM-COLOR-GEVK			Assembly variant:	State:
Sensors & actuators			ARX3A0CSSM090110	released
Revision: rev0.3	Repository revision: Not in version control		ON Semiconductor	
Engineer: T. Duris	18. March 2021 16:58			
File: ULP_BLE_Camera_sensors_actuators.SchDoc	4/10			
			Solution Engineering Center Piestany	



# SECO-RSL10-CAM-COLOR-GEVK

Image sensor processor (ISP) - power

Revision: rev0.3

Repository revision: Not in version control

Engineer: T. Duris

18. March 2021 16:58

File: ULP\_BLE\_Camera\_ISP\_power.SchDoc

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Assembly variant:

ARX3A0CSSM090110

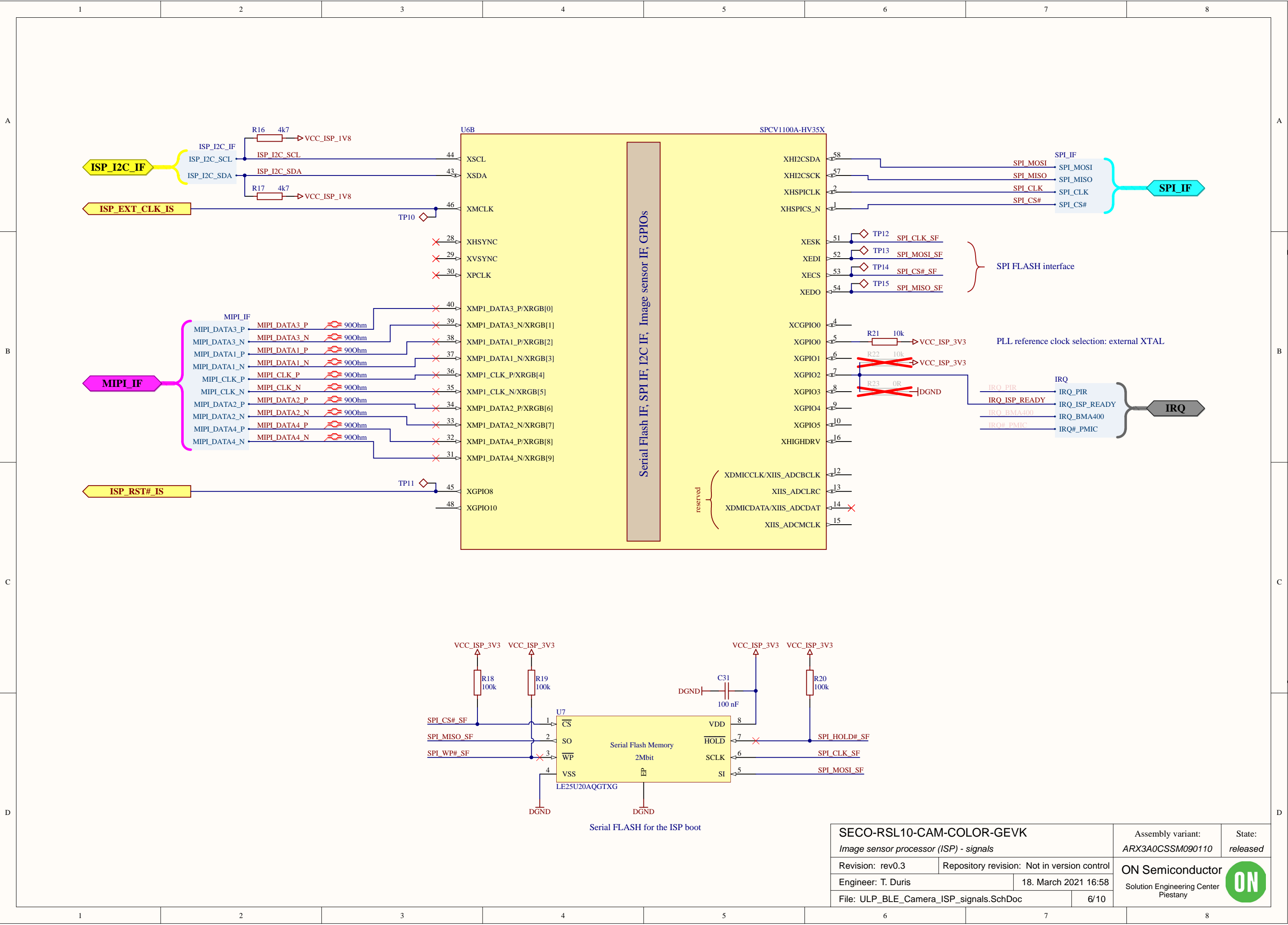
State:

released

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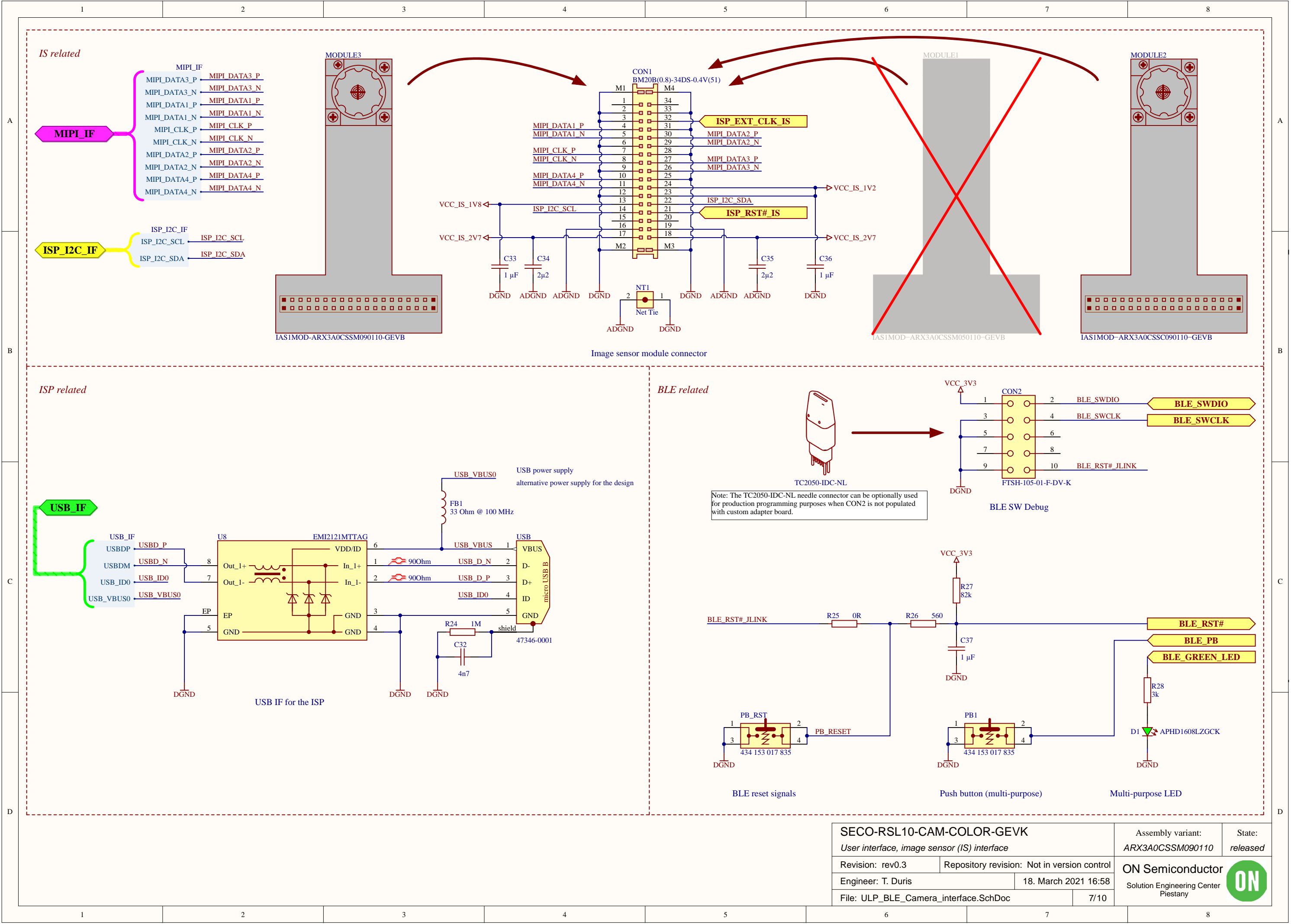


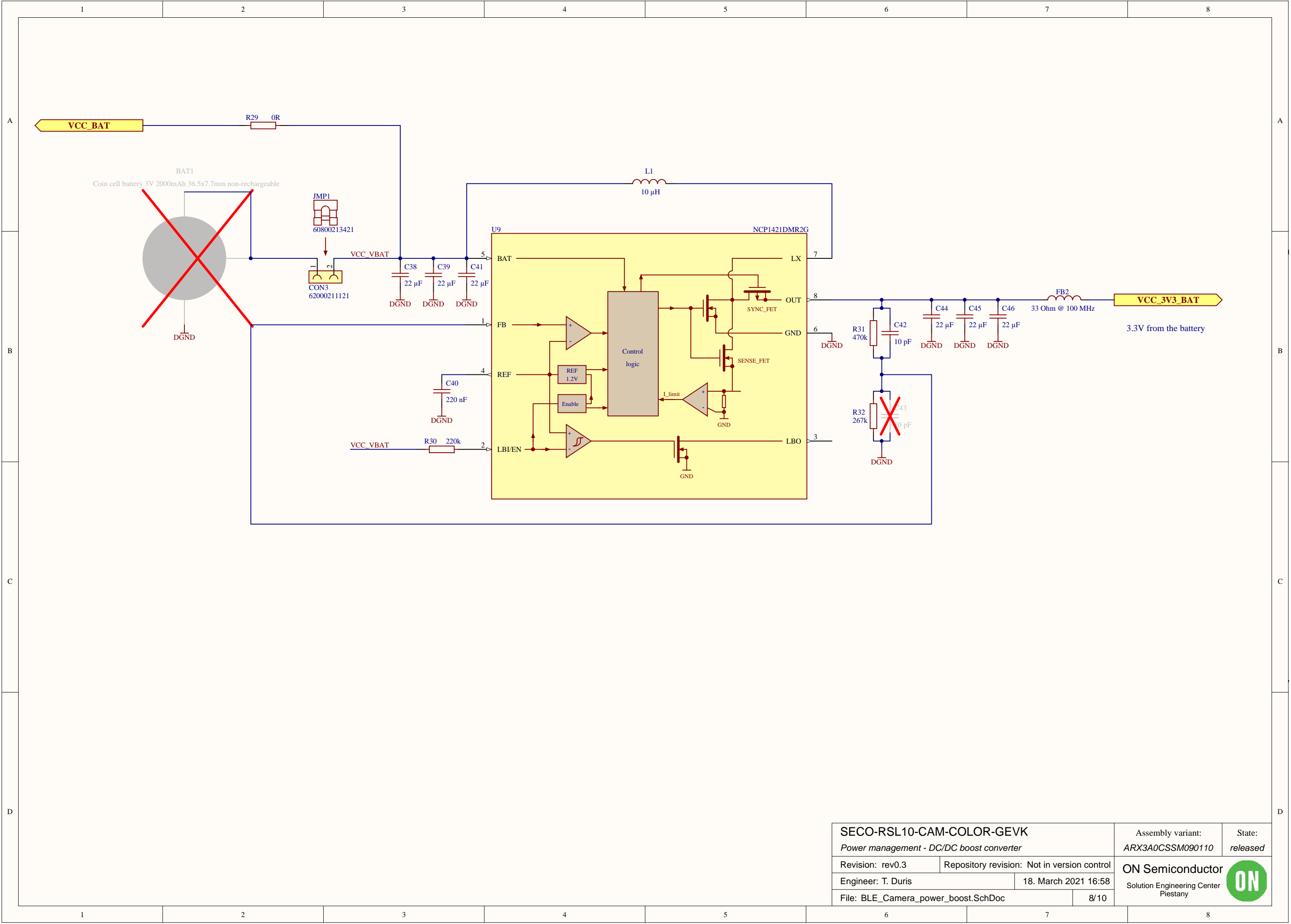
SECO-RSL10-CAM-COLOR-GEVK		Assembly variant:	State:
Image sensor processor (ISP) - signals		ARX3A0CSSM090110	released
Revision: rev0.3	Repository revision: Not in version control		
Engineer: T. Duris		18. March 2021 16:58	
File: ULP_BLE_Camera_ISP_signals.SchDoc		6/10	

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SECO-RSL10-CAM-COLOR-GEVK

Power management - DC/DC boost converter

Revision: rev0.3

Repository revision: Not in version control

Engineer: T. Duris

18. March 2021 16:58

File: BLE\_Camera\_power\_boost.SchDoc

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Assembly variant:

ARX3A0CSSM090110

State:

released

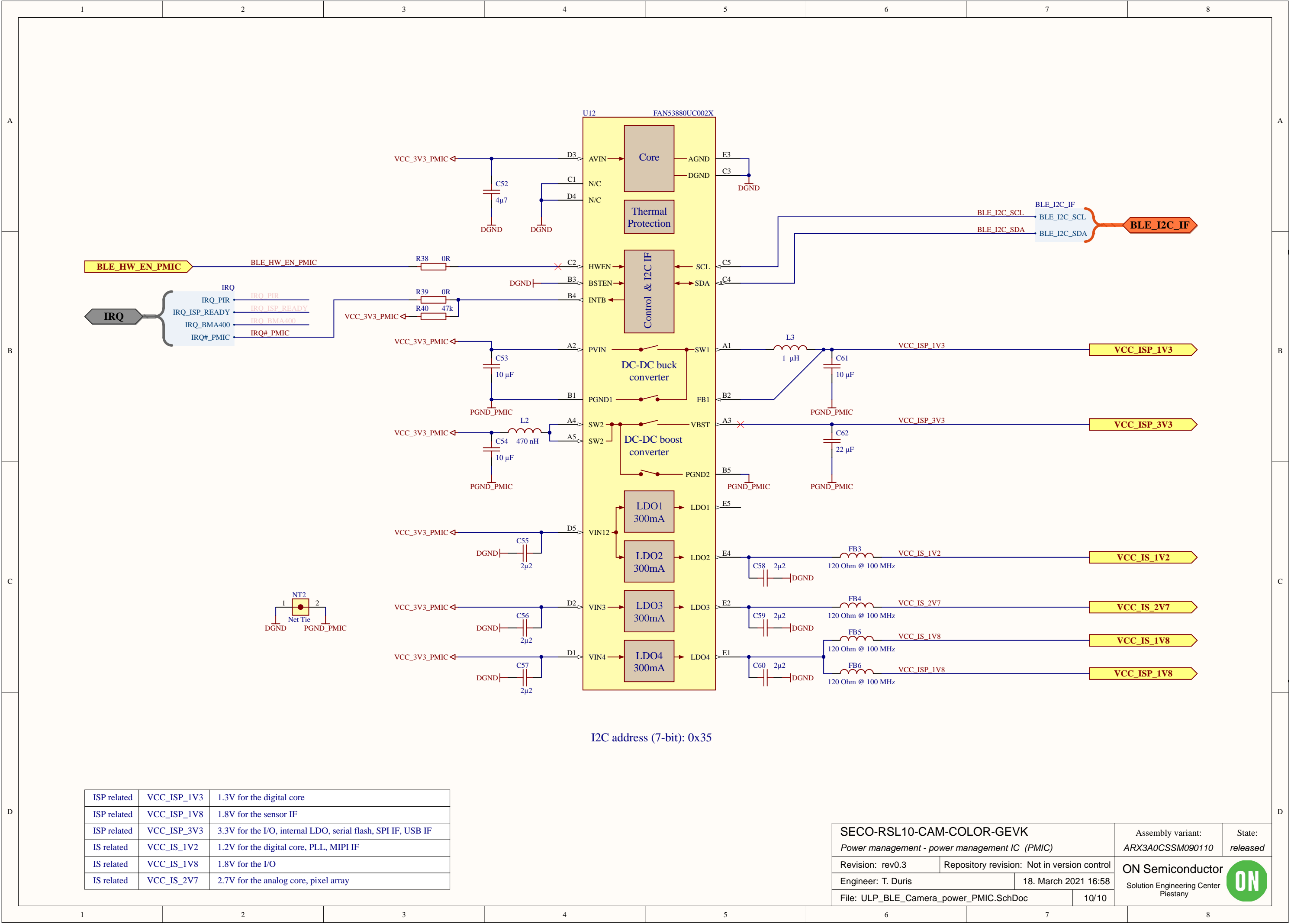
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ISP related	VCC_ISP_1V3	1.3V for the digital core
ISP related	VCC_ISP_1V8	1.8V for the sensor IF
ISP related	VCC_ISP_3V3	3.3V for the I/O, internal LDO, serial flash, SPI IF, USB IF
IS related	VCC_IS_1V2	1.2V for the digital core, PLL, MIPI IF
IS related	VCC_IS_1V8	1.8V for the I/O
IS related	VCC_IS_2V7	2.7V for the analog core, pixel array

SECO-RSL10-CAM-COLOR-GEVK

Power management - power management IC (PMIC)

Revision: rev0.3

Repository revision: Not in version control

Engineer: T. Duris

18. March 2021 16:58

File: ULP\_BLE\_Camera\_power\_PMIC.SchDoc

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Assembly variant:

ARX3A0CSSM090110

State:

released

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