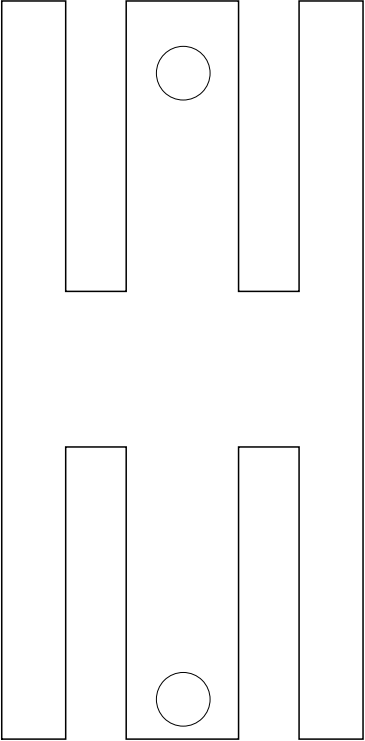



View from Top side (Scale 1.8) 2/2

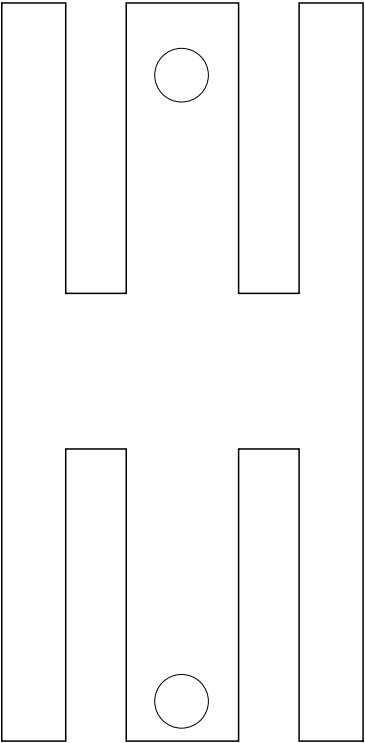


- ① Only components present in this assembly variant are listed in BoM table on the left
- ② Components not present in this assembly variant are crossed out by red line

|   |                         |   |   |
|---|-------------------------|---|---|
| IC_PAD  |                         | Revision:<br>0.1  | State:<br>in work   |
| Variant name: standard_board                  |                         | Assembly<br>document  | Sheet<br>1 / 4  |
| Engineer: zbjtdn                              | Date: ??.??? ???? ??:?? |   |   |
| PCB File: ON_Semiconductor_2_sided_PCB.PcbDoc |                         | ON Semiconductor<br>Solution Engineering Center<br>Piestany |  |
| Repository revision: locally modified 1251    |                         |   |   |



View from Bottom side (Scale 1.8)



- 3 CHA & CHB have to be properly oriented according their polarity !
- 4 Only components present in this assembly variant are listed in BoM table on the left
- 5 Components not present in this assembly variant are crossed out by red line

|   |                         |   |                   |
|---|-------------------------|---|-------------------|
| IC_PAD  |                         | Revision:<br>0.1  | State:<br>in work |
| Variant name: standard_board                  |                         | Assembly<br>document  | Sheet<br>2 / 4    |
| Engineer: zbjtdn                              | Date: ??.??? ???? ??:?? |   |                   |
| PCB File: ON_Semiconductor_2_sided_PCB.PcbDoc |                         | <div>ON Semiconductor</div> <div>Solution Engineering Center<br/>Piestany</div> <div>ON</div> |                   |
| Repository revision: locally modified 1251    |                         |   |                   |



1

2

3

4

5

A

A

B

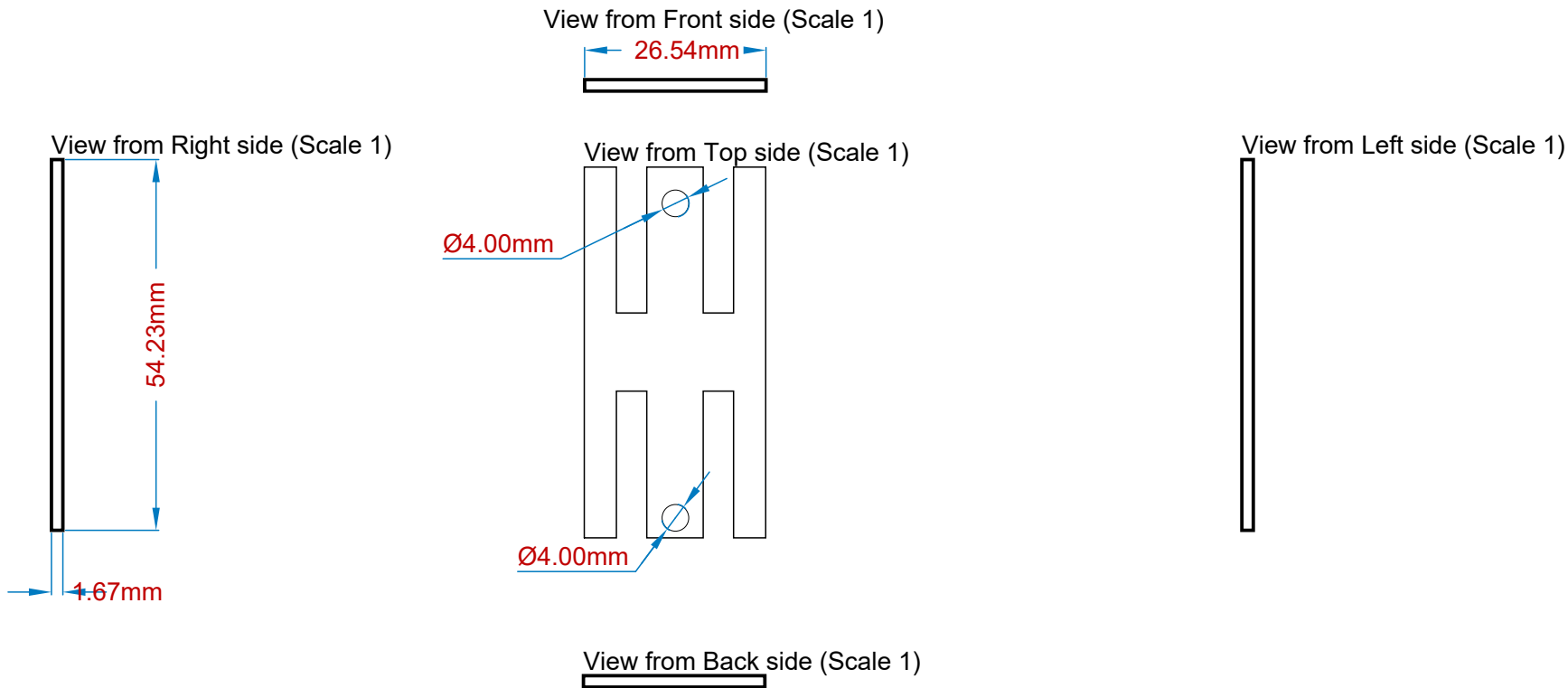
B


C

C

D

D



|   |                         |   |   |
|---|-------------------------|---|---|
| IC_PAD  |                         | Revision:<br>0.1  | State:<br>in work   |
| Variant name: standard_board                  |                         | Assembly<br>document  | Sheet<br>3 / 4  |
| Engineer: zbjtdn                              | Date: ??.??? ???? ??:?? |   |   |
| PCB File: ON_Semiconductor_2_sided_PCB.PcbDoc |                         | ON Semiconductor<br>Solution Engineering Center<br>Piestany |  |
| Repository revision: locally modified 1251    |                         |   |   |



1

2

3

4

5

A

A

B

B

C

C

D

D

Realistic View



|   |                         |  |                   |
|---|-------------------------|--|-------------------|
| IC_PAD  |                         | Revision:<br>0.1   | State:<br>in work |
| Variant name: standard_board                  |                         | Assembly<br>document   | Sheet<br>4 / 4    |
| Engineer: zbjtdn                              | Date: ??.??? ???? ??:?? |  |                   |
| PCB File: ON_Semiconductor_2_sided_PCB.PcbDoc |                         | <b>ON Semiconductor</b><br>Solution Engineering Center<br>Piestany |                   |
| Repository revision: locally modified 1251    |                         |  |                   |

