



DEFENSE LOGISTICS AGENCY
DLA LAND AND MARITIME
POST OFFICE BOX 3990
COLUMBUS, OH 43218-3990

IN REPLY REFER TO DLA LAND AND MARITIME-VQ-11-021283 (Mr. Yu/614-692-0588) November 22, 2010

SUBJECT: Level Q Certification and Eligibility for Qualification Expansion, MIL-PRF-38535,
FSC 5962, Microcircuits

Keenan Evans
Vice President, Quality, Reliability, and EHS
ON Semiconductor, Inc.
2300 Buckskin Road
Pocatello, Idaho 83201

Dear Mr. Evans:

ON Semiconductor has demonstrated to the DLA Land and Maritime that it complies with MIL-PRF-38535, the performance specification used by the Department of Defense for monolithic integrated circuits.

The previous Level Q Certification VQ-08-016683 granted to ON Semiconductor by the DLA Land and Maritime is superseded by this certification to reflect the current certification status of ON Semiconductor as documented on the Quality Management Plan (QMP) #12MQM97077A, Rev. BD, dated 17 September 2010.

In addition, the parts that are manufactured using the certified technology flows are being listed on the QML-38535. This will allow ON Semiconductor to mark parts with "Q" or "QML". These designators have been authorized by the Department of Defense for parts that have been produced to a QML specification, (i.e., one which allows less government oversight), the use of world-wide commercial production lines, reduced finished product testing based on statistical process controls (SPC), and other cost advantages.

Testing must be performed using the facilities and methods listed in the Laboratory Suitability letter DLA Land and Maritime -VQC-10-020882, or at facilities approved by the ON Semiconductor Technical Review Board or BU-CAB using its MIL-PRF-38535 Quality Management Program Plan.

This certification is subject to the conditions in DoD 4120.24-M, Defense Standardization Program.

Any and all of the facilities mentioned on the enclosure are subject to an audit by the Qualifying Activity at any time. The manufacturer shall be responsible for all audit expenses incurred for the offshore facilities.

QPL/QML manufacturers shall notify the qualifying activity immediately after learning of a potential issuance of a GIDEP alert, problem advisory or major quality/reliability problem on their QPL/QML products. Failure to provide prior notification may be grounds for removal from QML-38535.

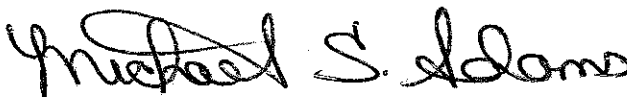
Finally, it is requested that the following activities be reported promptly to DLA Land and Maritime:

1. Changes to certified facilities, process flows, or approved testing subcontractors
2. Problem evaluation and a corrective action when:
 - a. A Technology Conformance Inspection (TCI) failure has been validated
 - b. The reliability of shipped parts is questionable.
3. Test optimization, including:
 - a. Implementation - paragraph J.3.12, Appendix J, MIL-PRF-38535
 - b. Changing, suspending or canceling a prior test optimization
4. Additions or deletions of parts in the QML-38535
5. Change of company QML contact or other key QML personnel

This certification is valid until terminated by written notice from the qualifying activity. If warranted, it may be withdrawn by this Center at any time.

If you have any questions, please contact Mr. Shing Yu at 614-692-0588.

Sincerely,



for JOSEPH GEMPERLINE
Chief
Sourcing and Qualification Division

Enclosure

cc: ON Semiconductor (Mona Hale)
DLA Land and Maritime -VQC (Michael Grammens)
DLA Land and Maritime -VQC (Scott Thomas)

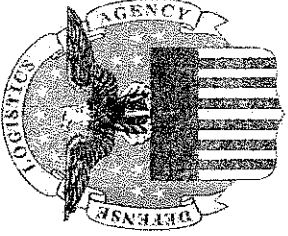
Enclosure to DLA LAND AND MARITIME-VQ-11-021283

<u>OPERATION FACILITY</u>		<u>LOCATION</u>	<u>TECHNOLOGY</u>	<u>FLOW NUMBER</u>
Wafer	ON Semiconductor	Gresham, OR	0.13um CMOS	7019030
Fabrication:	ON Semiconductor	Pocatello, ID	0.35, 0.5, 0.6, 0.8, 1.0, 1.25, 1.5um CMOS	7019030
Design:	ON Semiconductor	Pocatello, ID	Custom ASICs	1000001
	ON Semiconductor	Plano, TX	Custom ASICs	12MQE00004A
	ON Semiconductor	San Jose, CA	Custom ASICs	12MQE00004A
	ON Semiconductor	Lower Gwynedd, PA	Custom ASICs	12MQE00004A
	ON Semiconductor	San Diego, CA	Custom ASICs	12MQE00004A
	ON Semiconductor	American Fork, UT	Custom ASICs	12MQE00004A
	ON Semiconductor	Munich, Germany	Custom ASICs	12MQE00004A
Mask Fab:	Controlled by Wafer Fab		Custom ASICs	7619163
Hermetic	Pantronix	Fremont, CA	Ceramic	7020040
Assembly:	Pantronix	Fremont, CA	Hot Solder Dip	7050013
	Kyocera	San Diego, CA	DBGA Ceramic	7020040
	Crtieria Labs	Austin, TX	Ceramic	7020040
	Crtieria Labs	Austin, TX	Hot Solder Dip	7050013
	Amkor	Metro Manila,	Ceramic	7020040
	Technology	Philippines		
Plastic	Amkor	Seoul, Korea	PQFP Plastic	7020037
Assembly:	Technology			
	Amkor	Inchon, Korea	LQFP Plastic	7020037
	Technology			
	Amkor	Buchon, Korea	LQFP Plastic	7020037
	Technology			
	Amkor	Metro Manila,	LQFP, PLCC,	7020037 and
	Technology	Philippines	and PQFP Plastic	7020062

Test/Screen/TCI: See DLA Land and Maritime -VQC-10-020882 Laboratory Suitability letter.

Lead range for hermetic package families for the flows listed above include:
chip carrier (pre-J up to 84 leaded), chip carrier (leadless up to 84 terminals), flat pack (100-352 leads), side-braze (28-48 leads), pin grid array (69-181 pins), and DBGA (228-277 balls).

Lead range for plastic package families for the flows listed above include:
flat pack (44-240 leads), chip carrier (28-84 leads), LFPBGA (84 balls max), BGA (564 balls max), and SBGA (560 balls max).



**MIL-PRF-38535
CUSTOM MICROCIRCUIT CERTIFICATION**

**FOR
CLASSES Q and N
IS HEREBY AWARDED TO**

**ON SEMICONDUCTOR, INC.
2300 Buckskin Road
Pocatello, ID 83201**

THIS CERTIFICATION IS VALID UNTIL TERMINATED BY WRITTEN NOTIFICATION FROM DLA LAND AND MARITIME. REFERENCE DLA LAND AND MARITIME LETTER VQ(VQC-11-021283) FOR DETAILS PERTAINING TO THIS CERTIFICATION.

SAMUEL E. MERRITT
Director, Operations Support Directorate
DLA Land and Maritime