

Ultra96 (Xilinx ZU3) Camera Mezzanine

Features

- ✓ Industry standard 96Boards form factor for development
- ✓ Xilinx ZU3 based design
- ✓ Mezzanine contains ON's AP1302 providing the ISP function for the Xilinx FPGA
- ✓ Mezzanine enables ability to connect up to x2 ON Semiconductor IAS modules
- ✓ Demo shows: IAS1MOD-AR0144CSSM080110-GEVB

System Solution

- ✓ Complete solution to enable customers to develop their own solution on Xilinx ZU3 FPGA
- ✓ Mezzanine board optimized for ON Semiconductor components
- ✓ Voltage Regulators
 - NCP133, NCP114
- ✓ Load Switch
 - FPF1107
- ✓ Diodes
 - BAS16, NRVTSAF360

Current Proposition

- ✓ Ultra96 mezzanine enables short time to market developments of imaging solutions based on Xilinx ZU3