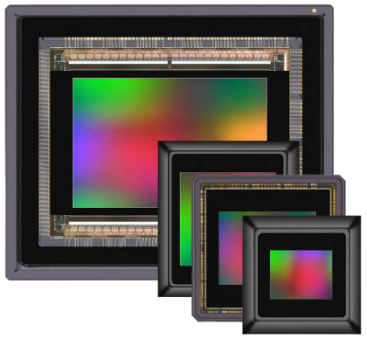
XGS Image Sensor Family



Integrated portfolio of high performance, low noise image sensors

- High bandwidth, lower power architecture
- Industry-leading image quality
- Resolutions to 45 Mp
- One camera design supports multiple resolutions and configurations

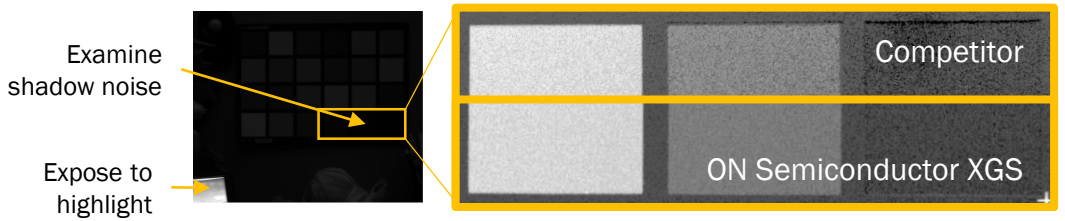
General Purpose Machine Vision

ITS

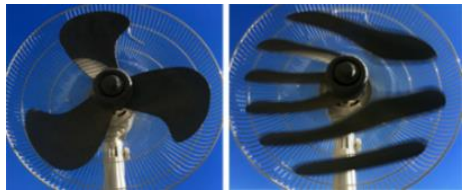
Broadcast

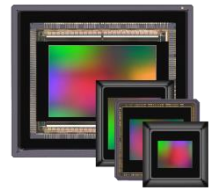
XGS Advantages

3.2 µm pixel size	→	High resolution at optical format
Advanced pixel design	→	Low noise, high image quality
Global Shutter	→	Capture moving objects without motion artifacts
Low-power, compact design	→	Up to 16 Mp in 29 x 29 mm^2 camera
Common IP, Shared product packages	→	One camera design for multiple resolutions



Similar noise profile under low light

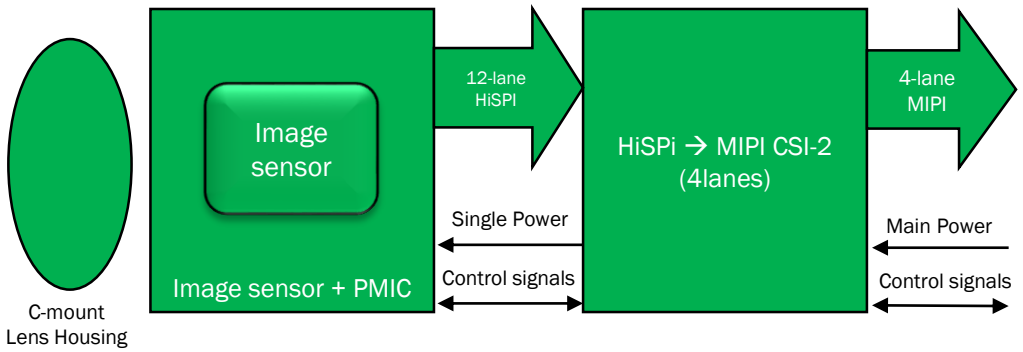




X-Cube

XGS Reference Design for 29 x 29 mm² Camera

- FPGA board to convert to MIPI output
- IAS interface to Demo 3 and DevWare
- C-mount lens holder
- Design files simplify adoption / camera design

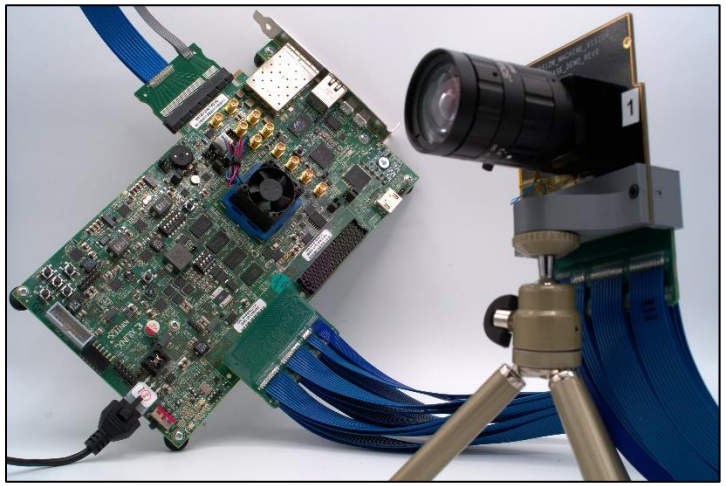


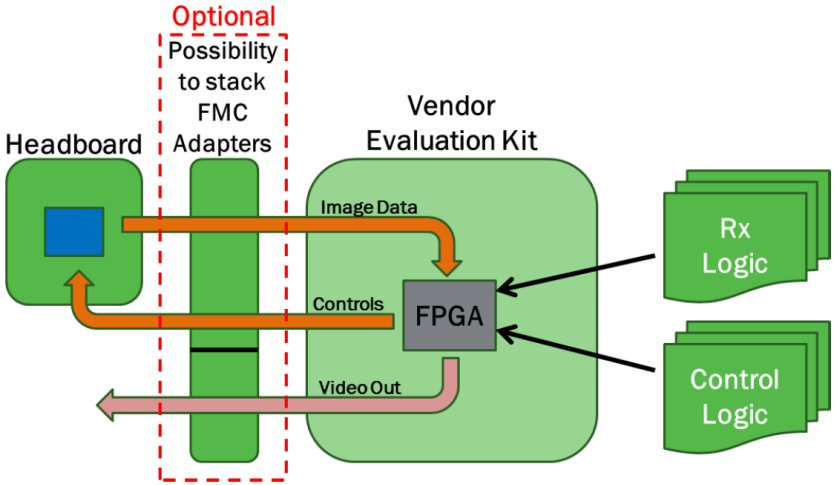
- 12-lane XGS 12000 to 4-lane MIPI
- Up to 28 fps
- Master/Slave mode
- Single +5 V Power supply

X-Celerator

XGS Development for FPGA Evaluation Environments

- Compatible with ANSI / VITA 57.1 FMC standard
- Xilinx RTL code, adaptable to Altera and others
- C-mount lens holder
- Design files simplify adoption / camera design





- 24-lane (full speed) implementation
- Master / Slave modes
- Only 2 HiSPi clocks (top + bottom)
- Single +5 V power supply