

製品概要

MC100EP56: Multiplexer, 2:1 Differential, Dual ECL, 3.3 V / 5.0 V

技術情報は、データシートをご参照ください。

The MC10/100EP56 is a dual, fully differential 2:1 multiplexer. The differential data path makes the device ideal for multiplexing low skew clock or other skew sensitive signals. Multiple VBB pins are provided. The VBB pin, an internally generated voltage supply, is available to this device only. For single-ended input conditions, the unused differential input is connected to VBB as a switching reference voltage. VBB may also rebias AC coupled inputs. When used, decouple VBB and VCC via a 0.01µF capacitor and limit current sourcing or sinking to 0.5mA. When not used, VBB should be left open. The device features both individual and common select inputs to address both data path and random logic applications. The 100 Series contains temperature compensation.

特長

- 360ps Typical Propagation Delays
- Maximum Frequency > 3 GHz Typical
- PECL Mode Operating Range: VCC = 3.0 V to 5.5 V with VEE = 0 V
- NECL Mode Operating Range: VCC = 0 V with VEE = -3.0 V to -5.5 V
- Open Input Default State
- Safety Clamp on Inputs
- Separate and Common Select
- Q Output will default LOW with inputs open or at VEE
- VBB Outputs
- Pb-Free Packages are Available

For more features, see the data sheet

アプリケーション

- Ideal for multiplexing low skew clock or other skew sensitive signals

電氣的仕様

製品	Compliance	Status	Input/Output Ratio	Channels	Input Level	Output Level	V _{CC} Typ (V)	f _{Max} Typ (MHz)	t _{Jitter} Typ (ps)	t _{skew(OO) Max} (ps)	t _{pd} Typ (ns)	Package Type
MC100EP56DTG	Pb-free	Active	2:1	2	CML	ECL	3.3	3000	0.2	100	0.36	TSSOP-20
	Halide free				ECL		5					
MC100EP56DTR2G	Pb-free	Active	2:1	2	ECL	ECL	3.3	3000	0.2	100	0.36	TSSOP-20
	Halide free				CML		5					
MC100EP56DWG	Pb-free	Active	2:1	2	CML	ECL	3.3	3000	0.2	100	0.36	SOIC-20W
	Halide free				ECL		5					
MC100EP56MNG	Pb-free	Active	2:1	2	ECL	ECL	3.3	3000	0.2	100	0.36	QFN-20
	Halide free				CML		5					

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