

製品概要

ESD8004: ESD Protection Array, USB 3.0

技術情報は、データシートをご参照ください。



The ESD8004 is designed to protect high speed data lines from ESD. Ultra low capacitance and low ESD clamping voltage make this device an ideal solution for protecting voltage sensitive high speed data lines. The flow through style package allows for easy PCB layout and matched trace lengths necessary to maintain consistent impedance between high speed differential lines such as USB 3.0.

特長

- 0.35pF max capacitance
- Deep snap back SCR technology

利点

- Supports data rates up to 6Gb/s
- Ultra low clamping voltage for ESD events

アプリケーション

- USB3.0
- eSATA
- Display Port

最終製品

- Computing
- Gaming

電氣的仕様

製品	Pricing (\$/Unit)	Compliance	Status	Interface	Number of Lines	Direction	C Max (pF)	V _(BR) Min (V)	V _{RWM} Max (V)	I _R Max (μA)	P _{PK} Max (W)	Package Type
ESD8004MUTAG	0.208	Pb-free	Active	USB 3.0	4	Unidirectional	0.35	5.5	3.3	1	-	UDFN-10
		Halide free		USB 3.1								
				eSATA 3.0								
				Display Port								
SZESD8004MUTAG	0.2288	AEC Qualified PPAP Capable Pb-free Halide free	Active	Display Port USB 3.0 eSATA 3.0 USB 3.1	4	Unidirectional	0.35	5.5	3.3	1	-	UDFN-10

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

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