

製品概要

CAV25512H: 512-Kb SPI Serial CMOS EEPROM - Automotive Grade

技術情報は、データシートをご参照ください。

The CAV25512H is a 512-Kb Serial CMOS EEPROM device internally organized as 64Kx8 bits. This features a 128-byte page write buffer and supports the Serial Peripheral Interface (SPI) protocol. The device is enabled through a Chip Select (CS) input. In addition, the required bus signals are clock input (SCK), data input (SI) and data output (SO) lines. The HOLD input may be used to pause any serial communication with the CAV25512H device. The device features software and hardware write protection, including partial as well as full array protection. On-Chip ECC (Error Correction Code) makes the device suitable for high reliability applications.

特長

- Automotive Temperature Grade 1 (-40°C to +125°C)
 - 2.5 V to 5.5 V Supply Voltage Range
 - 10 MHz SPI Compatible
 - SPI Modes (0,0) & (1,1)
 - 128-byte Page Write Buffer
 - Additional Identification Page with Permanent Write Protection
 - Self-timed Write Cycle
 - Hardware and Software Protection
 - Block Write Protection - Protect ¼, ½ or Entire EEPROM Array
 - Low Power CMOS Technology
- For more features, see the data sheet

利点

- Allows the use in safety critical application
- Single power supply from 2.5 V to 5.5 V. Functional over the entire temperature range

アプリケーション

- Door module
- Seat control unit
- Body computer & energy management unit
- Immobilizer unit
- Wiper module

詳細は、弊社 www.onsemi.jp の営業または販売代理店にお問い合わせください。

1/28/2020 作成